



JNTU Warangal
WARANGAL - 506 009 (A.P.) INDIA
CONSOLIDATED MEMORANDUM OF MARKS

No. **039849**

Date: **31-05-2015**

Examination : **B. TECH (ELECTRONICS & COMMN.), APRIL, 2015**

Candidate's Name : **THATIKANTI MAHESH**

Father's Name : **THATIKANTI KANKAIAH**

Roll No. : **1101711537**

SUBJECT	MAX. MARKS	PASS MARKS	MARKS SECURED	MONTH & YEAR OF PASSING
I YEAR:				
COMMUNICATIVE ENGLISH	150	53	84	JUN, 2012
MATHEMATICS - I	150	53	131	JUN, 2012
APPLIED PHYSICS	150	53	100	JUN, 2012
ENGINEERING CHEMISTRY	150	53	133	JUN, 2012
ENGINEERING MECHANICS	150	53	104	JUN, 2012
PROGRAMMING CONCEPTS & INFORMATION TECH.	150	53	130	JUN, 2012
ENGINEERING GRAPHICS	150	53	109	JUN, 2012
WORK SHOP PRACTICE LAB.	75	26	63	JUN, 2012
APPLIED PHYSICS LAB.	75	26	68	JUN, 2012
ENGINEERING CHEMISTRY LAB.	75	26	72	JUN, 2012
COMPUTER PROGRAMMING LAB.	75	26	69	JUN, 2012
ENVIRONMENTAL STUDIES *	100	35	59	JUN, 2012
II YEAR, I SEMESTER:				
MATHEMATICS - II	150	53	128	NOV, 2012
ELECTRONIC MEASUREMENT & INSTRUMENTATION	150	53	120	NOV, 2012
NETWORK ANALYSIS & SYNTHESIS	150	53	98	NOV, 2012
SWITCHING THEORY & LOGIC DESIGN	150	53	124	NOV, 2012
ELECTRONIC DEVICES & CIRCUITS - I	150	53	134	NOV, 2012
ELECTRICAL TECHNOLOGY	150	53	118	NOV, 2012
ELECTRONIC DEVICES & CIRCUITS - I LAB.	75	26	72	NOV, 2012
ELECTRICAL TECHNOLOGY & NETWORKS LAB.	75	26	65	NOV, 2012
II YEAR, II SEMESTER:				
MATHEMATICS - III	150	53	103	MAY, 2013
ELECTRO MAGNETIC WAVES & TRANSM. LINES	150	53	88	MAY, 2013
DIGITAL INTEGRATED CIRCUITS	150	53	95	MAY, 2013
ELECTRONIC DEVICES & CIRCUITS - II	150	53	93	MAY, 2013
SIGNALS & SYSTEMS	150	53	118	MAY, 2013
PROGRAMMING & DATA STRUCTURES	150	53	82	MAY, 2013
ELECTRONIC DEVICES & CIRCUITS - II LAB.	75	26	73	MAY, 2013
DIGITAL ELECTRONICS LAB	75	26	68	MAY, 2013
III YEAR, I SEMESTER:				
ECONOMICS, MANAGEMENT & ACCOUNTANCY	150	53	104	OCT, 2013
COMPUTER ARCHITECTURE	150	53	101	OCT, 2013
ANALOG COMMUNICATION SYSTEMS	150	53	121	OCT, 2013
CONTROL SYSTEM ENGINEERING	150	53	131	OCT, 2013
PULSE AND DIGITAL CIRCUITS	150	53	116	OCT, 2013
LINEAR INTEGRATED CIRCUITS	150	53	107	OCT, 2013
PULSE AND DIGITAL CIRCUITS LAB	75	26	73	OCT, 2013
LINEAR INTEGRATED CIRCUITS LAB	75	26	73	OCT, 2013

SUBJECT	MAX. MARKS	PASS MARKS	MARKS SECURED	MONTH & YEAR OF PASSING
III YEAR, II SEMESTER:				
ENTREPRENEURSHIP DEVELOPMENT	150	53	106	APR, 2014
ANTENNAS & WAVE PROPAGATION	150	53	118	APR, 2014
MICRO PROCESSORS & MICRO CONTROLLERS	150	53	119	APR, 2014
DIGITAL COMMUNICATION SYSTEMS	150	53	134	APR, 2014
TV AND RADAR ENGINEERING	150	53	93	APR, 2014
DIGITAL SIGNAL PROCESSING	150	53	150	APR, 2014
ANALOG & DIGITAL COMMUNICATION SYS. LAB.	75	26	75	APR, 2014
MICRO PROCESSORS & MICRO CONTROLLERS LAB	75	26	74	APR, 2014
IV YEAR, I SEMESTER:				
MICRO WAVE ENGINEERING	150	53	97	NOV, 2014
OPTICAL & SATELLITE COMMUNICATION SYSTEMS	150	53	137	NOV, 2014
VLSI DESIGN	150	53	96	NOV, 2014
DIGITAL IMAGE PROCESSING	150	53	87	NOV, 2014
ECAD LAB.	75	26	72	NOV, 2014
MICRO WAVE ENGINEERING LAB.	75	26	68	NOV, 2014
PROJECT	50	18	47	NOV, 2014
IV YEAR, II SEMESTER:				
CELLULAR AND MOBILE COMMUNICATION	150	53	130	APR, 2015
DATA COMMUNICATION NETWORKS	150	53	127	APR, 2015
EMBEDDED SYSTEM DESIGN	150	53	104	APR, 2015
DSP LAB	75	26	68	APR, 2015
PROJECT WORK & SEMINAR	250	88	234	APR, 2015
TOTAL MARKS : 4283 / 5400 --) Marks of (I Yr+II Yr)/2 + (III Yr+IV Yr)				
DIVISION : FIRST DIVISION WITH DISTINCTION				

SECTION IN CHARGE

SUPDT/ASST. REGISTRAR

CONTROLLER OF EXAMINATIONS