



**OFFICIAL TRANSCRIPT**

Ref. No. 9577395/1

Dated 10 Aug 2018

This is to certify that **Mr./Ms. Rakesh Devgan S/o/D/o Sh. Surinder Kumar Sharma** has passed **Bachelor of Technology (Electronics & Communication Engg. Lateral Entry)** from **Sri Sukhmani Institute of Engineering & Technology, Derabassi** an affiliated college of this University, under University Roll No. **1422934**. The detail of marks are given as under:-

THIRD Semester 104046717 Nov-2016				FOURTH Semester 104390701 Nov-2017			
Subject Name	TP	OM	MM	Subject Name	TP	OM	MM
Engineering Mathematics-III (BTAM-301)	T	40	100	Analog Communication Systems (BTEC-401)	T	44	100
Object Oriented Programming Using C++ (BTCS-305)	T	43	100	Signal & Systems (BTEC-402)	T	49	100
Analog Devices & Circuits (BTEC-301)	T	49	100	Electromagnetics & Antennas (BTEC-403)	T	44	100
Digital Circuit and Logic Design (BTEC-302)	T	54	100	Electronic Measurement & Instrumentation (BTEC-404)	T	43	100
Network Analysis and Synthesis (BTEC-303)	T	41	100	Pulse Wave Shaping and Switching (BTEC-405)	T	50	100
Lab Analog Devices & Circuits (BTEC-304)	P	31	50	Lab Analog Communication Systems (BTEC-406)	P	39	50
Lab Digital Circuit and Logic Design (BTEC-305)	P	39	50	Lab Electronic Measurement & Instrumentation (BTEC-407)	P	32	50
Lab Object Oriented Programming (BTEC-309)	P	37	50	Lab Signal & Systems using MATLAB/Mentor DSP (BTEC-408)	P	38	50
<b>TOTAL</b>		<b>334</b>	<b>650</b>	Linear Control Systems (BTEE-402)	T	44	100
FIFTH Semester 104404722 Nov-2017				SIXTH Semester 104513955 April-2018			
Subject Name	TP	OM	MM	Subject Name	TP	OM	MM
Data Structures (BTCS-304)	T	54	100	Operating Systems (BTCS-401)	T	59	100
Digital Communication System (BTEC-501)	T	45	100	Microwave & Radar Engineering (BTEC-601)	T	54	100
Digital Signal Processing (BTEC-502)	T	43	100	Wireless Communication System (BTEC-602)	T	49	100
Linear Integrated Circuit (BTEC-503)	T	53	100	Engineering Economics & Industrial Management (BTEC-603)	T	64	100
Microprocessors & Microcontrollers (BTEC-504)	T	49	100	VLSI Design (BTEC-604)	T	48	100
Lab Digital Signal Processing (BTEC-505)	P	34	50	Lab VLSI (BTEC-605)	P	31	50
Lab Linear Integrated Circuit (BTEC-506)	P	33	50	Lab Microwave Engineering (BTEC-606)	P	35	50
Lab Digital Communication System (BTEC-507)	P	37	50	Information Theory & Coding (BTEC-907)	T	70	100
Lab Hardware Programme & Interfacing (BTEC-508)	P	37	50	General Fitness (GF)	P	91	100
Industrial Training (Ind. Trg-5)	P	72	100	<b>TOTAL</b>		<b>501</b>	<b>800</b>
<b>TOTAL</b>		<b>457</b>	<b>800</b>	EIGHTH Semester 104113826 April-2017			
SEVENTH Semester 104066851 Nov-2016				Subject Name			
Subject Name	TP	OM	MM	TP	OM	MM	
Computer Networks (BTCS-403)	T	51	100	Software Training (ST/BTCH-701/BTCS-801/BTIT-801/BTEE-701/BTME-IT/BTCE-701)	P	226	250
Embedded Systems (BTEC-701)	T	52	100	Industrial Oriented Project /Industrial /Institutional Training (ST/BTCH-702/BTCS-802/BTIT-802/BTEE-702/BTME-IT/BTCE-702)	P	468	500
Optical Communication (BTEC-702)	T	53	100	<b>TOTAL</b>		<b>694</b>	<b>750</b>
Lab Wireless and Optical Systems & Networks (BTEC-703)	P	35	50				
Lab Embedded Systems (BTEC-704)	P	40	50				
Major Project (BTEC-705)	P	116	150				
Satellite Communication (BTEC-910)	T	60	100				
Wireless Sensor Network (BTEC-920)	T	60	100				
General Fitness (GF)	P	88	100				
<b>TOTAL</b>		<b>555</b>	<b>850</b>				

Total Marks Obtained: **3010**

Total Maximum Marks: **4700**

Note:- Medium of Instruction is English

EDP Cell

Prepared By:

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Checked By:

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AR(Exams)

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Dy. Controller of Exams

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Controller of Examinations

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 2. This document can be verified online by scanning 2D bar-code (top right corner) using smart phone having internet connection.