



# Dr Karthikeyan P

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## Expertise

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### Telecommunications

Electronics and Communication Engineering

## Work experience

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1. PSNA College of Engineering and Technology 2008 — Present

Associate Professor

Dindigul

## Education

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1. PhD., - 2016

Anna University

## Publication

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1. Image synthesis M/2D/HWT in VLSI technology

Maniraj Kumar P.;Thirumurugan P.;Karthikeyan P.;Saravanan S.;Kumarnath J.  
International Journal of Innovative Technology and Exploring Engineering, Volume 9, Year 2019, Pages 2976-2982

2. **Analytical modeling and simulation of dual material double gate all around tunnel field effect transistor using MATLAB**

R Helen Ramya Bharathi ., P. Karthikeyan .,  
Proceedings of 2017 International Conference on Innovations in Information, Embedded and Communication Systems, ICII ECS 2017, Volume 2018-January, Year 2018, Pages 1-7

3. **Ultra low consumption device for future electronics-Tunnel field effect transistor : A survey**

R Helen Ramya Bharathi ., T. Evangeline Santhia ., P. Karthikeyan .,  
Proceedings - 2017 IEEE International Conference on Electrical, Instrumentation and Communication Engineering, ICEICE 2017, Volume 2017-December, Year 2017, Pages 1-5

4. **Domino in adiabatic logic circuits for brain tumor in MRI signal processing**

Karthikeyan P.;Saravanan R.  
Journal of Pure and Applied Microbiology, Volume 9, Year 2015, Pages 103-110

5. **Analytical modeling of dual material surrounding gate TFET to reduce short-channel effects for low power applications**

Karthikeyan P.;Saravanan R.;Venkatesh M.;Vinothkumar K.  
International Journal of Applied Engineering Research, Volume 10, Year 2015, Pages 513-518

6. **Power optimization in domino circuits using current mirror based leakage compensating keeper**

P. Karthikeyan ., R. Saravanan ., N. Saravanan .,  
Applied Mechanics and Materials, Volume 573, Year 2014, Pages 169-175