**भारतीय मानक** *Indian Standard* **IS 15382 (Part 1) : 2022 IEC 60664-1 : 2020**

[*Superseding* IS 2165 (Part 1) : 1977]

निम्न-वोल्टता तंत्र में उपकरणों का विद्युतरोधी **यु समन््वय**

**भाग 1 सिद््धाांत, अपेक्षाए और परी ं क्षण**

*( दूसरा पुनरीक्षण )* 

# **Insulation Coordination for Equipment within Low-Voltage Systems**

**Part 1 Principles, Requirements and Tests**

*( Second Revision )*

ICS 29.080.30

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# NATIONAL FOREWORD

This Indian Standard (Part 1) (Second Revision) which is identical with IEC 60664-1 : 2020 'Insulation coordination for equipment within low-voltage systems — Part 1: Principles, requirements and tests' was adopted by the Bureau of Indian Standards on recommendation of the High Voltage Engineering Sectional Committee and approval of the Electrotechnical Division Council.

After the publication of this standard, IS 2165 (Part 1) : 1977 shall be treated as withdrawn since the requirements given in IS 2165 (Part 1) : 1977 has been covered in this standard.

The text of IEC Standard has been approved as suitable for publication as an Indian Standard without deviations. Certain terminologies and conventions are, however, not identical to those used in Indian Standards. Attention is particularly drawn to the following:

- a) Wherever the words 'International Standard' appear referring to this standard, they should be read as 'Indian Standard'.
- b) Comma (,) has been used as a decimal marker, while in Indian Standards, the current practice is to use a point (.) as the decimal marker.

In this adopted standard, reference appears to certain International Standards for which Indian Standards also exist. The corresponding Indian Standards, which are to be substituted in their respective places, are listed below along with their degree of equivalence for the editions indicated:



The technical committee has reviewed the provisions of the following International Standards referred to in this adopted standard and has decided that they are acceptable for use in conjunction with this standard:



Only the English language text has been retained while adopting it in this Indian Standard, and as such, the page numbers given here are not the same as in the IEC Publication.

For the purpose of deciding whether a particular requirement of this standard is complied with the final value, observed or calculated expressing the result of a test or analysis shall be rounded off in accordance with IS 2 : 2022 'Rules for rounding off numerical values (*second revision*)'. The number of significant places retained in the rounded off value should be the same as that of the specified value in this standard.

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# **Indian Standard**

# INSULATION COORDINATION FOR EQUIPMENT **INSULATION COORDINATION FOR EQUIPMENT** WITHIN LOW-VOLTAGE SYSTEMS **WITH LOW SUPPLIN LOW LOW STATE Part 1: Principles, requirements and tests**

# **PART 1 PRINCIPLES, REQUIREMENTS AND TESTS**

# *( Second Revision )*

# <span id="page-6-0"></span>**1 Scope**

This part of IEC 60664 deals with **insulation coordination** for equipment having a **rated voltage** up to AC 1 000 V or DC 1 500 V connected to **low-voltage supply systems**.

This document applies to frequencies up to 30 kHz.

NOTE 1 Requirements for **insulation coordination** for equipment within **low-voltage supply systems** with rated frequencies above 30 kHz are given in IEC 60664-4.

NOTE 2 Higher voltages can exist in internal circuits of the equipment.

It applies to equipment for use up to 2 000 m above sea level and provides guidance for use at higher altitudes (See [5.2.3.4\)](#page-25-2).

It provides requirements for technical committees to determine **clearances**, **creepage distances** and criteria for **solid insulation**. It includes methods of electrical testing with respect to **insulation coordination**.

The minimum **clearances** specified in this document do not apply where ionized gases are present. Special requirements for such situations can be specified at the discretion of the relevant technical committee.

This document does not deal with distances:

- through liquid insulation;
- through gases other than air;
- through compressed air.

This basic safety publication focusing on safety essential requirements is primarily intended for use by technical committees in the preparation of standards in accordance with the principles laid down in IEC Guide 104 and ISO/IEC Guide 51.

One of the responsibilities of a technical committee is, wherever applicable, to make use of basic safety publications in the preparation of its publications.

However, in case of missing specified values for **clearances**, **creepage distances** and requirements for **solid insulation** in the relevant product standards, or even missing standards, this document applies.

# <span id="page-6-1"></span>**2 Normative references**

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60068-2-2, *Environmental testing – Part 2-2: Tests – Tests B: Dry heat*

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IEC 60068-2-14:2009, *Environmental testing – Part 2-14: Tests – Test N: Change of temperature*

IEC 60068-2-78, *Environmental testing – Part 2-78: Tests – Test Cab: Damp heat, steady state*

IEC 60270, *High-voltage test techniques – Partial discharge measurements*

IEC 61140:2016*, Protection against electric shock – Common aspects for installation and equipment*

IEC 61180:2016, *High-voltage test techniques for low-voltage equipment – Definitions, test and procedure requirements, test equipment*

### <span id="page-7-0"></span>**3 Terms, definitions and abbreviated terms**

For the purposes of this document, the following terms and definitions apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

#### <span id="page-7-1"></span>**3.1 Terms and definitions**

#### **3.1.1**

#### **low-voltage supply system**

all installations and plant provided for the purpose of generating, transmitting and distributing electricity

[SOURCE: IEC 60050-601:1985, 601-01-01, modified – The term " electric power system" has been replaced with "low-voltage supply system".]

#### **3.1.2**

#### **mains supply**

AC or DC power distribution system (external to the equipment) that supplies operating power to the equipment

Note 1 to entry: **Mains supply** includes public or private utilities and, unless otherwise specified in this document, equivalent sources such as motor-driven generators and uninterruptible power supplies.

#### **3.1.3**

#### **insulation coordination**

mutual correlation of insulation characteristics of electrical equipment taking into account the expected **micro-environment** and other influencing stresses

Note 1 to entry: Expected voltage stresses are characterized in terms of the characteristics defined in [3.1.7](#page-8-0) to [3.1.16.](#page-9-0) 

[SOURCE: IEC 60050-442:2014, 442-09-01, modified – "electrical" replaces "electric" and Note 1 to entry has been added.]

#### **3.1.4**

#### **clearance**

shortest distance in air between two conductive parts

[SOURCE: IEC 60050-581:2008, 581-27-76]

# **3.1.5**

#### **creepage distance**

shortest distance along the surface of a solid insulating material between two conductive parts

[SOURCE: IEC 60050-151:2001, 151-15-50]

# **3.1.6**

#### **solid insulation**

solid insulating material or a combination of solid insulating materials, placed between two conductive parts or between a conductive part and a body part

[SOURCE: IEC 60050-903:2015, 903-04-14, modified – The example has been deleted.]

# <span id="page-8-0"></span>**3.1.7**

#### **working voltage**

highest RMS value of the AC or DC voltage across any particular insulation which can occur when the equipment is supplied at **rated voltage**

Note 1 to entry: **Transient overvoltages** are disregarded.

Note 2 to entry: Both open-circuit conditions and normal operating conditions are taken into account.

[SOURCE: IEC 60050-851:2008, 851-12-31]

# **3.1.8**

#### **steady-state working voltage**

**working voltage** after the **transient overvoltage** phenomena have subsided and not taking into account short-term voltage variations

# **3.1.9**

#### **steady-state peak voltage** peak value of the **steady-state working voltage**

#### <span id="page-8-1"></span>**3.1.10 recurring peak voltage**

*U*rp

maximum peak value of periodic excursions of the voltage waveform resulting from distortions of an AC voltage or from AC components superimposed on a DC voltage

Note 1 to entry: Random **overvoltages**, for example due to occasional switching, are not considered to be recurring peak voltages.

[SOURCE: IEC 60050-442:2014, 442-09-15]

# **3.1.11**

**overvoltage** 

<electrical system> any voltage having a peak value exceeding the corresponding peak value of maximum **steady-state working voltage** at normal operating conditions

#### **3.1.12**

#### **temporary overvoltage**

**overvoltage** at power frequency of relatively long duration

[SOURCE: IEC 60050-614:2016, 614-03-13, modified – "power frequency overvoltage" has been replaced with "**overvoltage** at power frequency" and Note 1 to entry has been deleted.]

# **3.1.13**

#### **transient overvoltage**

short duration **overvoltage** of a few milliseconds or less, oscillatory or non-oscillatory, usually highly damped

[SOURCE: IEC 60050-614:2016, 614-03-14, modified – "overvoltage with a duration" has been replaced with "short duration overvoltage" and the notes have been deleted.]

# **3.1.14**

#### **withstand voltage**

<in an electrical system> voltage to be applied to a specimen under specified test conditions which does not cause breakdown of insulation and/or **flashover** of a satisfactory specimen

# **3.1.15**

#### **impulse withstand voltage**

highest peak value of impulse voltage of specified form and polarity which does not cause breakdown of insulation under specified conditions

[SOURCE: IEC 60050-442:2014, 442-09-18, modified – "prescribed" has been replaced with "specified".]

### <span id="page-9-0"></span>**3.1.16**

#### **temporary withstand overvoltage**

highest RMS value of a **temporary overvoltage** which does not cause breakdown of insulation under specified conditions

[SOURCE: IEC 60050-442:2014, 442-09-19]

# <span id="page-9-1"></span>**3.1.17**

# **rated voltage**

*U*n

<of equipment> value of voltage assigned by the manufacturer, to a component, device or equipment and to which operation and performance characteristics are referred

Note 1 to entry: Equipment may have more than one **rated voltage** value or may have a **rated voltage** range.

[SOURCE: IEC 60050-442:2014, 442-09-10, modified – "rated value of the voltage" has been replaced with "value of voltage" and Note 2 to entry has been deleted.]

#### <span id="page-9-2"></span>**3.1.18 rated insulation voltage**

 $U_i$ 

[value](http://www.electropedia.org/iev/iev.nsf/display?openform&ievref=151-16-08) of the RMS **withstand voltage** assigned by the manufacturer to the equipment or to a part of it, characterizing the specified (long-term) withstand capability of its insulation

Note 1 to entry: The **rated insulation voltage** is equal to or greater than the **rated voltage** of equipment which is primarily related to functional performance.

[SOURCE: IEC 60050-312:2014, 312-06-02, modified – symbol has been added, in the definition "rated value" has been replaced by "value" and in the note "not necessarily equal to" has been replaced by "equal to or greater than".]

#### <span id="page-9-3"></span>**3.1.19**

#### **rated impulse withstand voltage**

*U*imp

value of the **impulse withstand voltage** assigned by the manufacturer to the equipment or to a part of it, characterizing the specified withstand capability of its insulation against **transient overvoltages**

# **3.1.20 overvoltage category**

numeral defining a **transient overvoltage** condition

Note 1 to entry: **Overvoltage categories** I, II, III and IV are used, see [4.3.2.](#page-17-4) 

[SOURCE: IEC 60050-581:2008, 581-21-02, modified – Note 1 to entry has been added.]

# **3.1.21**

# **environment**

<of an electrical system> surrounding which can affect performance of a device or system

EXAMPLE Pressure, temperature, humidity, **pollution**, radiation and vibration.

### **3.1.22**

#### **macro-environment**

**environment** of the room or other location in which the equipment is installed or used

[SOURCE: IEC 60050-442:2014, 442-01-55]

### **3.1.23**

#### **micro-environment**

<of an electrical system> ambient conditions which immediately influences the dimensioning of the **clearance** and **creepage distances**

# **3.1.24**

# **pollution**

 $\leq$  of an electrical system> any condition of foreign matter, solid, liquid or gaseous (ionized gases), that can affect dielectric strength or surface resistivity

#### **3.1.25**

#### **pollution degree**

numeral characterizing the expected **pollution** of the **micro-environment**

[SOURCE: IEC 60050-581:2008, 581-21-07, modified – Note 1 to entry has been deleted.]

# <span id="page-10-1"></span>**3.1.26**

#### **homogeneous field**

electric field which has an essentially constant voltage gradient between electrodes

Note 1 to entry: The **homogeneous field** condition is referred to as case B in [Table](#page-67-0) F.2 and [Table F.8.](#page-73-0) See als[o4.9.](#page-23-0) 

#### <span id="page-10-0"></span>**3.1.27**

# **inhomogeneous field**

#### non-uniform field

electric field which does not have an essentially constant voltage gradient between electrodes

Note 1 to entry: See also [4.9.](#page-23-0)

[SOURCE: IEC 60050-442:2014, 442-09-03, modified – "inhomogeneous electric field" has been replaced with "inhomogeneous field", Note 1 to entry has been replaced with a new Note 1 and Note 2 to entry has been deleted.]

#### **3.1.28**

#### **electric insulation**

part of an electrotechnical product which separates the conducting parts at different electrical potentials during operation or insulates such parts from the surroundings

[SOURCE: IEC 60050-212:2010, 212-11-07, modified –"electric" has been replaced with "electrical".]

# **3.1.29**

#### **functional insulation**

insulation between conductive parts which is necessary only for the proper functioning of the equipment

[SOURCE: IEC 60050-195:1998, 195-02-41, modified – "necessary" has been replaced with "which is necessary only".]

#### **3.1.30**

#### **basic insulation**

insulation of hazardous-live-parts which provides basic protection

Note 1 to entry: This concept does not apply to insulation used exclusively for functional purposes.

[SOURCE: IEC 60050-826:2004, 826-12-14]

#### **3.1.31**

#### **supplementary insulation**

independent insulation applied in addition to **basic insulation** for fault protection

[SOURCE: IEC 60050-826:2004, 826-12-15]

#### **3.1.32**

#### **double insulation**

insulation comprising both **basic insulation** and **supplementary insulation**

[SOURCE: IEC 60050-826:2004, 826-12-16]

#### **3.1.33**

#### **reinforced insulation**

insulation of hazardous-live-parts which provides a degree of protection against electric shock equivalent to **double insulation**

Note 1 to entry: **Reinforced insulation** may comprise several layers which cannot be tested singly as **basic insulation** or **supplementary insulation**.

[SOURCE: IEC 60050-826:2004, 826-12-17]

#### <span id="page-11-1"></span>**3.1.34 partial discharge PD** electric discharge that partially bridges the insulation

Note 1 to entry: A **partial discharge** may occur inside the insulation or adjacent to a conductor.

Note 2 to entry: Scintillations of low energy on the surface of insulating materials are often described as **partial discharges** but should rather be considered as disruptive discharges of low energy, since they are the result of local dielectric breakdowns of high ionization density, or small arcs, according to the conventions of physics.

[SOURCE: IEC 60050-442:2014, 442-09-05, modified – Notes 1 and 2 to entry have been added.]

# <span id="page-11-0"></span>**3.1.35 apparent charge**

*q*app

electric charge which can be measured at the terminals of the specimen under test

Note 1 to entry: The **apparent charge** is smaller than the **partial discharge**.

Note 2 to entry: The measurement of the **apparent charge** requires a short-circuit condition at the terminals of the specimen (see Clause [D.2\)](#page-62-2) under test.

[SOURCE: IEC 60050-442:2014, 442-09-06, modified – In the Note 2 to entry, "(see Clause D.2)" has been added.]

# **3.1.36**

#### **specified discharge magnitude**

magnitude of the **apparent charge** when this is regarded as the limiting value

Note 1 to entry: The pulse with the maximum amplitude should be evaluated.

[SOURCE: IEC 60050-442:2014, 442-09-07]

#### **3.1.37**

#### **pulse repetition rate**

average number of pulses per second with an **apparent charge** higher than the detection level

Note 1 to entry: Within the scope of this document, it is not permitted to weigh discharge magnitudes according to the **pulse repetition rate**.

#### **3.1.38**

#### **partial discharge inception voltage**

lowest peak value of the test voltage at which the **apparent charge** becomes greater than the **specified discharge magnitude** when the test voltage is increased above a low value for which no discharge occurs

Note 1 to entry: For AC tests the RMS value may be used.

[SOURCE: IEC 60050-212:2014, 212-11-41]

# **3.1.39**

#### **partial discharge extinction voltage**

<of an electrical system> lowest peak value of the test voltage at which the **apparent charge** becomes less than the **specified discharge magnitude** when the test voltage is reduced below a high level where such discharges have occurred

Note 1 to entry: For AC tests, the RMS value may be used.

#### **3.1.40**

#### **partial discharge test voltage**

peak value of the voltage in a **partial discharge** test, where the **apparent charge** is less than the **specified discharge magnitude**

Note 1 to entry: For AC tests, the RMS value may be used.

[SOURCE: IEC 60050-212:2014, 212-11-62, modified – Note 1 to entry has been deleted, Note 2 to entry is renumbered as Note 1 to entry.]

# **3.1.41**

#### **type test**

<of an electrical system> test made on one or more devices representative to a certain design to check the conformity to the specifications

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# **3.1.42**

### **routine test**

conformity test made on each individual item during or after manufacture

[SOURCE: IEC 60050-151:2001, 151-16-17]

# **3.1.43**

# **sampling test**

test on a number of devices taken at random from a batch

[SOURCE: IEC 60050-411:1996, 411-53-05, modified – "machine" has been replaced with "device".]

# **3.1.44**

# **electric breakdown**

failure of insulation under electric stress when the discharge completely bridges the insulation, thus reducing the voltage between the electrodes almost to zero

# **3.1.45**

#### **sparkover**

<of an electrical system> **electric breakdown** in a gaseous or liquid medium

### **3.1.46**

#### **flashover**

**electric breakdown** between conductors in a gas or a liquid or in vacuum, at least partly along the surface of **solid insulation**

[SOURCE: IEC 60050-212:2010, 212-11-47]

# **3.1.47 puncture electric breakdown** through **solid insulation**

[SOURCE: IEC 60050-614:2016, 614-03-17, modified – "disruptive discharge" has been replaced with "electric breakdown" and "dielectric" has been replaced with "insulation".]

# <span id="page-13-0"></span>**3.2 Abbreviated terms**

List of terms with abbreviated terms and symbols together with the corresponding terminological entry:



# <span id="page-13-1"></span>**4 Basic technical characteristics for insulation coordination**

# <span id="page-13-2"></span>**4.1 General**

**Insulation coordination** requires the selection of the **electric insulation** technical characteristics of the equipment with regard to its application and in relation to its surroundings and environmental conditions.

**Insulation coordination** represents one aspect of the safety of persons, livestock and property, so that the probability of risk of incidents due to voltage stresses does not lead to an unacceptable risk of harm.

This document addresses **insulation coordination** for any kind of hazard. Technical committees shall take into account the concepts of **clearance, creepage distance** and **solid insulation** as well as the concepts of **functional insulation, basic insulation, supplementary insulation**, **double insulation** and **reinforced insulation** for the specific hazard considering the nature of the hazard.

The technical committee shall make a risk assessment to identify the hazard in case of fault of **functional insulation**. In case a failure of **functional insulation** could create an unacceptable risk of harm (for example, due to mechanical conditions, ageing behaviour), **functional insulation** shall at a minimum be designed according to the requirements of **basic insulation**, after which another risk assessment is performed to assess the residual risk. See [5.2](#page-23-7) to [5.4.](#page-30-0) In case a fault of **functional insulation** does not create any hazard, the technical committee may choose not to apply the requirements of this basic safety publication.

NOTE See ISO/IEC Guide 51 and IEC Guide 116 for further details about risk assessment and unacceptable risk of harm.

**Electric insulation** technical characteristics cover:

- voltages across the insulation according to [4.2;](#page-14-0)
- **overvoltage categories** according to [4.3;](#page-17-2)
- frequency according to [4.4;](#page-18-1)
- **pollution degree** according to [4.5;](#page-18-4)
- insulation materials according to [4.6;](#page-19-2)
- environmental aspects according to [4.7](#page-22-0) (e.g. altitude see [4.7.2,](#page-22-2) temperature see [4.7.3,](#page-22-3) vibrations see [4.7.4,](#page-22-4) humidity see [4.7.5,](#page-22-5) duration see [4.8\)](#page-22-6);
- field distribution according to [4.9.](#page-23-0)

**Insulation coordination** can only be achieved if the design of the equipment is based on the stresses to which it is likely to be subjected during its intended life.

#### <span id="page-14-0"></span>**4.2 Voltages**

### <span id="page-14-1"></span>**4.2.1 General aspects**

When considering insulation performances, the following aspects are relevant:

- the voltages which can appear within the system:
	- **transient overvoltages** according to [4.2.2](#page-15-0) and **overvoltage category** according to [4.3;](#page-17-2)
	- **temporary overvoltages** according to [4.2.3.](#page-16-0)
- the voltages generated by the equipment (which could adversely affect other equipment in the system):
	- **transient overvoltage** according to [4.2.2;](#page-15-0)
	- **recurring peak voltage** according to [4.2.4;](#page-16-1)
	- **steady-state working voltage** according to [4.2.5;](#page-17-0)
	- **steady-state peak voltage** according to [4.2.6.](#page-17-1)

# <span id="page-15-0"></span>**4.2.2 Transient overvoltages**

# <span id="page-15-1"></span>**4.2.2.1 General**

To apply the concept of **insulation coordination**, **transient overvoltage** shall be taken into consideration. The **transient overvoltages** that shall be considered are:

- **transient overvoltages** generated by atmospheric disturbances (for example indirect lightning strikes) and transmitted by the **mains supply** distribution system;
- **transient overvoltages** generated due to switching of loads in the **mains supply**;
- **transient overvoltages** generated by external circuits;
- **transient overvoltages** generated internally in the equipment.

**Insulation coordination** uses a preferred series of values of impulse voltages. The preferred impulse voltages are:

330 V, 500 V, 800 V, 1 500 V, 2 500 V, 4 000 V, 6 000 V, 8 000 V, 12 000 V.

**Insulation coordination** with regard to **transient overvoltage** is based on controlled **overvoltage** conditions. There are two kinds of control:

- inherent control: the condition within an electrical system wherein the characteristics of the system can be expected to limit the prospective **transient overvoltages** to a defined level;
- protective control: the condition within an electrical system wherein specific **overvoltage** attenuating means can be expected to limit the prospective **transient overvoltages** to a defined level.

See also [Table](#page-54-1) B.1 and [Table](#page-55-0) B.2.

# <span id="page-15-3"></span>**4.2.2.2 Transient overvoltages entering through the mains supply**

To determine the expected transients generated by atmospheric disturbances or due to switching of loads in the **mains supply**, the **rated voltage** (*U*n) and the **overvoltage category** are normally used as the basis to determine the required **impulse withstand voltage**.

For equipment subjected to **transient overvoltages** that exceed the **impulse withstand voltage** these **transient overvoltages** shall be taken into account.

# **4.2.2.3 Transient overvoltages generated by external circuits**

The applicable value of the **transient overvoltage** that may occur on any external circuit (for example coaxial cable or twisted pair networks) shall be determined. Where more than one external circuit is present, the highest **transient overvoltage** applies.

If the external circuit **transient overvoltages** are known to be higher than the one from the **overvoltage category** typically defined for such kind of equipment, the highest value of these known **transient overvoltages** shall be used.

# <span id="page-15-2"></span>**4.2.2.4 Transient overvoltages generated internally in the equipment**

For the equipment likely to generate an **overvoltage** that is higher than the transients expected to come into the equipment, for example due to switching devices, the required impulse voltage shall take into account the transient generated in the equipment. The value of these **transient overvoltages** generated internally in the equipment shall be used without considering the preferred list in [4.2.2.1.](#page-15-1)

# **4.2.2.5 Attenuation of transient overvoltage levels**

Equipment or parts of equipment may be used under conditions where the transients are reduced. Various technology of component exists such as surge protective device (SPD), transformer, capacitance, resistance, and can have different behaviour regarding the **transient overvoltage** attenuation. These various technologies shall be verified and the method of propagation measurement shall be defined based on their relevant product standard.

Attention is drawn to the fact that a surge protective device within the installation or within equipment may have to dissipate more energy than a surge protective device at the origin of the installation having a higher protection level (clamping voltage). This applies particularly to the surge protective device with the lowest protection level (clamping voltage).

In case attenuation of the transient is expected, the **transient overvoltage** across the insulation may be measured by applying the required impulse test to the equipment and measuring the actual remaining transient over the insulation, see [6.7.](#page-43-0) The measured value may be used as the expected **transient overvoltage**. While performing the test, the equipment is energized at **rated voltage** and transients of both polarities shall be considered.

### <span id="page-16-0"></span>**4.2.3 Temporary overvoltages**

Due to faults on the **mains supply**, **temporary overvoltages** between lines and earth/neutral of several seconds will be generated and shall be considered when applying the concept of **insulation coordination**.

**Insulation coordination** with regard to **temporary overvoltages** is based on the **temporary overvoltage** specified in IEC 60364-4-44:2007, Clause 442. The values of **temporary overvoltage** in low-voltage equipment due to an earth fault in the high-voltage system are given in [5.4.3.2.](#page-31-0)

# <span id="page-16-1"></span>**4.2.4 Recurring peak voltage**

Due to the intended operation modes of specific products, internally generated voltages may also include recurring peaks superimposed to the **working voltage**. These recurring peaks voltages shall be considered when applying the concept of **insulation coordination**.

**Insulation coordination** with regard to **recurring peak voltage** shall consider that **partial discharges** can occur in **solid insulation** (see [4.6.2.3\)](#page-20-1) or along surfaces of insulation (see [Table F.9\)](#page-73-1).

**Recurring peak voltage** has a waveshape which is measured by an oscilloscope of sufficient bandwidth, from which the peak amplitude is determined according to [Figure 1.](#page-17-5)



**Key**

- A Steady-state voltage value
- B **Steady-state peak voltage**
- <span id="page-17-5"></span>C **Recurring peak voltage**

### **Figure 1 – Recurring peak voltage**

# <span id="page-17-0"></span>**4.2.5 Steady-state working voltage**

The highest **steady-state working voltage** (RMS value of the AC or DC value) across the insulation with the equipment supplied at the **rated voltage** shall be considered. This kind of **steady-state working voltage** can be lower, equal or higher than the **rated voltage** of the equipment. The **steady-state working voltage** of internal circuits is a direct consequence of the design of products and might be significantly higher than the value of **rated voltage**.

#### <span id="page-17-1"></span>**4.2.6 Steady-state peak voltage**

The peak value of **steady-state working voltage** across the insulation with the equipment supplied at the **rated voltage** shall be considered. The **steady-state peak voltage** of internal circuits is a direct consequence of the design of products.

# <span id="page-17-2"></span>**4.3 Overvoltage categories**

#### <span id="page-17-3"></span>**4.3.1 General**

The concept of **overvoltage categories** is used for equipment energized directly from the **mains supply**.

The **overvoltage categories** have a probabilistic implication rather than the meaning of physical attenuation of the **transient overvoltage** downstream in the installation.

NOTE This concept of **overvoltage categories** is used in Clause 443 of IEC 60364-4-44:2007 and IEC 60364-4-44:2007/AMD1:2015.

A similar concept can also be used for equipment connected to other systems, for example telecommunication and data systems.

#### <span id="page-17-4"></span>**4.3.2 Equipment energized directly from the mains supply**

Technical committees shall specify the **overvoltage category** as based on the following general explanation of **overvoltage categories**:

– Equipment of **overvoltage category** IV is for use at the origin of the installation.

NOTE 1 Examples of such equipment are electricity meters, primary overcurrent protection devices and ripple control units.

– Equipment of **overvoltage category** III is equipment in fixed installations and for cases where the reliability and the availability of the equipment is subject to special requirements.

NOTE 2 Examples of such equipment are switches in the fixed installation and equipment for industrial use with permanent connection to the fixed installation.

– Equipment of **overvoltage category** II is energy-consuming equipment to be supplied from the fixed installation. If such equipment is subjected to special requirements with regard to reliability and availability, **overvoltage category** III applies.

NOTE 3 Examples of such equipment are appliances, portable tools and other household and similar loads.

– Equipment with an impulse withstand voltage corresponding to **overvoltage category** I shall not have direct connection to a **mains supply**.

Measures shall be taken to ensure that the **temporary overvoltages** that could occur are sufficiently limited so that their peak value does not exceed the relevant rated impulse voltage of [Table](#page-66-1) F.1.

NOTE 4 Unless the circuits are designed to take the **temporary overvoltages** into account, equipment of **overvoltage category** I cannot be directly connected to the **mains supply**.

#### <span id="page-18-0"></span>**4.3.3 Systems and equipment not energized directly from the mains supply**

It is recommended that technical committees specify **overvoltage categories** or **rated impulse withstand voltage** as appropriate. Application of the preferred series of [4.2.2.1](#page-15-1) is recommended.

NOTE Telecommunication or industrial control systems or independent systems on vehicles are examples of such systems.

#### <span id="page-18-1"></span>**4.4 Frequency**

#### <span id="page-18-2"></span>**4.4.1 General**

The voltage withstand capability of **clearances**, **creepage distances** and **solid insulation** will be reduced with increased frequency. This effect can be observed from 1 kHz. The design of **clearances**, **creepage distances** and **solid insulation** according to this document cover the effect of high-frequencies up to 30 kHz. For frequencies above 30 kHz, see [5.1.2.](#page-23-4)

# <span id="page-18-3"></span>**4.4.2 Solid insulation**

The frequency of the voltage influences the electric strength of the **solid insulation**. Dielectric heating and the probability of thermal instability increase approximately in proportion to the frequency. Increasing the frequency will reduce the electric strength of most insulating materials.

This condition has been observed in switched-mode power supplies where the insulation is subjected to repetitive voltage peaks at frequencies up to 500 kHz.

# <span id="page-18-4"></span>**4.5 Pollution**

# <span id="page-18-5"></span>**4.5.1 General**

The **micro-environment** determines the effect of **pollution** on the insulation. The **macroenvironment**, however, has to be taken into account when considering the **microenvironment**.

Means may be provided to reduce **pollution** at the insulation under consideration by effective use of enclosures, encapsulation or hermetic sealing. Such means to reduce **pollution** may not be effective when the equipment is subject to condensation or if, in normal operation, it generates pollutants itself. Enclosures for outdoor and indoor installation, intended for use in locations with high humidity and temperatures varying within wide limits, shall be provided with suitable arrangements (natural ventilation, forced ventilation, internal heating, drain holes, etc.) to prevent harmful condensation within the enclosure. Degrees of protection provided by enclosures (IP code), according to the classes specified in IEC 60529, do not necessarily improve the **micro-environment** with regard to **pollution**.

The technical committee shall provide information to verify, according to their standards, the performance of the enclosed system.

Small **clearances** can be bridged completely by solid particles, dust and water and therefore minimum **clearances** are specified where **pollution** may be present in the **microenvironment**.

### <span id="page-19-0"></span>**4.5.2 Degrees of pollution in the micro-environment**

For the purpose of evaluating **creepage distances** and **clearances**, the following four degrees of **pollution** in the **micro-environment** are established:

#### – **Pollution degree** 1

No **pollution** or only dry, non-conductive **pollution** occurs. The **pollution** has no influence.

#### – **Pollution degree** 2

Only non-conductive **pollution** occurs except that occasionally a temporary conductivity caused by condensation is to be expected. This condensation may occur during periods of on-off load cycles of the equipment.

#### – **Pollution degree** 3

Conductive **pollution** occurs or dry non-conductive **pollution** occurs which becomes conductive due to condensation which is to be expected.

#### – **Pollution degree** 4

Continuous conductivity occurs due to conductive dust, rain or other wet conditions.

# <span id="page-19-1"></span>**4.5.3 Conditions of conductive pollution**

The dimensions for **creepage distance** cannot be specified where permanently conductive **pollution** is present (**pollution degree** 4). For temporarily conductive **pollution** (**pollution degree** 3), the surface of the insulation may be designed to avoid a continuous path of conductive **pollution**, for example by means of ribs and grooves (see [5.3.3.7\)](#page-29-3).

#### <span id="page-19-2"></span>**4.6 Insulating material**

#### <span id="page-19-3"></span>**4.6.1 Solid insulation**

The concept of **insulation coordination** may be realized by an appropriate insulation material. The insulation behaviour of the **solid insulation** is directly affected by its intrinsic material characteristics. Electrical, mechanical and other stresses which might affect the insulation behaviour over the life time of the product shall be considered.

As the electric strength of **solid insulation** is considerably greater than that of air, less attention may be paid to the design of insulation systems. On the other hand, the insulating distances through solid insulating material are, as a rule, much smaller than the **clearances** so that high electric stresses result. Another point to be considered is that the high electric strength of material is seldom made use of in practice. In insulation systems, gaps may occur between electrodes and insulation and between different layers of insulation, or voids may be present in the insulation. **Partial discharges** can occur in these gaps or voids at voltages far

below the level of **puncture** and this may influence decisively the service life of the **solid insulation**. However, **partial discharges** are unlikely to occur below a peak voltage of 500 V.

Of equally fundamental importance is the fact that **solid insulation**, as compared with gases, is not a renewable insulating medium so that, for example, high voltage peaks which may occur infrequently can have a very damaging and irreversible effect on **solid insulation**. This situation can occur while in service and during routine high-voltage testing.

### <span id="page-20-0"></span>**4.6.2 Stresses**

# **4.6.2.1 General**

A number of detrimental influences accumulate over the service life of **solid insulation**. These follow complex patterns and result in ageing. Therefore, electrical and other stresses (e.g. thermal, environmental) are superimposed and contribute to ageing.

The long-term performance of **solid insulation** can be simulated by a short-term test in combination with suitable conditioning (see [6.4.3\)](#page-37-0).

There is a general relationship between the thickness of **solid insulation** and the aforesaid failure mechanisms. By a reduction of the thickness of **solid insulation** the field strength is increased and leads to a higher risk of failure. Due to the individual electrical characteristics of the materials it is not possible to calculate the required thickness of **solid insulation**. The performance can only be verified by testing.

# **4.6.2.2 Mechanical shock**

In the case of inadequate impact strength, mechanical shock may cause insulation failure. Failure from mechanical shock could also occur due to the reduced impact strength of materials:

- due to material becoming brittle when the temperature falls below its glass transition temperature;
- after prolonged exposure to a high temperature that has caused loss of plasticizer or degradation of the base polymer.

Technical committees shall consider this when specifying environmental conditions for transportation, storage, installation and use.

# <span id="page-20-1"></span>**4.6.2.3 Partial discharges (PD)**

Some types of **solid insulation** can withstand discharges, while others cannot. Voltage, repetition rate of discharges and discharge magnitude are important parameters.

NOTE Ceramic insulators are usually able to withstand **partial discharges**.

The PD behaviour is influenced by the frequency of the applied voltage. It is established from accelerated life tests at increased frequency that the time to failure is approximately inversely proportional to the frequency of the applied voltage. However, practical experience only covers frequencies up to 5 kHz since, at higher frequencies, other failure mechanisms may also be present, for example dielectric heating.

# <span id="page-20-2"></span>**4.6.2.4 Other stresses**

Many other stresses can damage insulation and their consequences need to be considered by technical committees.

Examples of such stresses include:

- radiation, both ultraviolet and ionizing;
- stress-crazing or stress-cracking caused by exposure to solvents or active chemicals;
- migration of plasticizers;
- the effect of bacteria, moulds or fungi;
- mechanical creep.

# <span id="page-21-0"></span>**4.6.3 Comparative tracking index (CTI)**

### <span id="page-21-1"></span>**4.6.3.1 Behaviour of insulating material in the presence of scintillations**

With regard to tracking, an insulating material can be roughly characterized according to the damage it suffers from the concentrated release of energy during scintillations when a surface leakage current is interrupted due to the drying-out of the contaminated surface. The following behaviour of an insulating material in the presence of scintillations can occur:

- decomposition of the insulating material;
- the wearing away of insulating material by the action of electrical discharges (electrical erosion);
- the progressive formation of conductive paths which are produced on the surface of insulating material due to the combined effects of electric stress and electrolytically conductive contamination on the surface (tracking).

NOTE Tracking or erosion will occur when:

- a liquid film carrying the surface leakage current breaks; and
- the applied voltage is sufficient to break down the small gap formed when the film breaks; and
- the current is above a limiting value which is necessary to provide sufficient energy locally to thermally decompose the insulating material beneath the film.

Deterioration increases with the time for which the current flows.

#### **4.6.3.2 Comparative tracking index (CTI) values to categorize insulating materials**

A method of classification for insulating materials according to [4.6.3.1](#page-21-1) does not exist. The behaviour of the insulating material under various contaminants and voltages is extremely complex. Under these conditions, many materials may exhibit two or even all three of the characteristics stated. A direct correlation with the material groups of [5.3.2.4](#page-27-1) is not practical. However, it has been found by experience and tests that insulating materials having a higher relative performance also have approximately the same relative ranking according to the comparative tracking index (CTI). Therefore, this document uses the comparative tracking index (CTI) values to categorize insulating materials.

# **4.6.3.3 Test for comparative tracking index (CTI)**

The test for comparative tracking index (CTI) in accordance with IEC 60112 is designed to compare the performance of various insulating materials under test conditions. It gives a qualitative comparison and in the case of insulating materials having a tendency to form tracks, it also gives a quantitative comparison.

#### **4.6.3.4 Non- tracking materials**

For glass, ceramics or other inorganic insulating materials which do not track, **creepage distances** need not be greater than their associated **clearance** for the purpose of **insulation coordination**.

# <span id="page-22-0"></span>**4.7 Environmental aspects**

# <span id="page-22-1"></span>**4.7.1 General**

The physical and geographical location of the equipment can affect the insulation system significantly. Environmental factors such as altitude, temperature, vibrations and humidity require consideration to ensure that the **insulation coordination** remains reliable over the life time of the equipment.

# <span id="page-22-2"></span>**4.7.2 Altitude**

The breakdown voltage of a **clearance** in air is, according to Paschen's law, proportional to the product of the distance between electrodes and the atmospheric pressure. The required distances for **clearances** in this document is corrected according to the difference in atmospheric pressure between 2 000 m and sea level for **homogeneous field** and **inhomogeneous field**. See [5.2.3.4](#page-25-2) for dimensioning of **clearance** for altitudes above 2 000 m and [6.2.2.1.4](#page-34-0) for altitude consideration when verifying **clearance** at altitudes different from 2 000 m.

# <span id="page-22-3"></span>**4.7.3 Temperature**

Temperature can cause:

- mechanical distortion due to the release of locked-in stress;
- softening of thermoplastics;
- embrittlement of some materials due to loss of plasticiser;
- softening of some cross-linked materials particularly if the glass transition temperature of the material is exceeded;
- increased dielectric losses leading to thermal instability and failure.

High temperature gradients, for example during short-circuits, may cause mechanical failure.

# <span id="page-22-4"></span>**4.7.4 Vibrations**

Mechanical stresses caused by vibration or shock during operation, storage or transportation may cause delamination, cracking or breaking-up of the insulating material (see [5.4.4.2\)](#page-32-3).

# <span id="page-22-5"></span>**4.7.5 Humidity**

The presence of water vapour can influence the insulation resistance and the **partial discharge** extinction voltage, aggravate the effect of surface contamination, produce corrosion and dimensional changes. For some materials, high humidity will significantly reduce the electric strength. Low humidity can be unfavourable in some circumstances, for example by increasing the retention of electrostatic charge and by decreasing the mechanical strength of some materials, such as polyamide.

# <span id="page-22-6"></span>**4.8 Duration of voltage stress**

The duration of voltage stress especially influences the long-term insulation behaviour of **creepage distance** and **solid insulation**. See [5.3.3.4](#page-28-1) for **creepage distances**.

For **clearance**, where the voltage will create a breakdown of the insulation instantaneously, the duration in general does not influence the **clearance**.

# <span id="page-23-0"></span>**4.9 Electrical field distribution**

The electrical field distribution influences the electric strength of insulation.

- The **inhomogeneous field** condition of a point-plane electrode configuration is the worst case with regard to voltage withstand capability. It is represented by a point electrode having a 30  $\mu$ m radius and a plane of 1 m  $\times$  1 m (see [3.1.27\)](#page-10-0).
- The **homogeneous field** distribution is the most favourable and theoretical case where the electrical field is completely homogeneous between two spheres (see [3.1.26\)](#page-10-1). Typically, to achieve a **homogeneous field** condition between two spheres, the radius of each sphere shall be greater than the distance between them. **Homogeneous field** conditions in real design are very difficult to achieve.

In fact, the electrical field distribution will normally be between **homogeneous field** and **inhomogeneous field**.

# <span id="page-23-1"></span>**5 Design for insulation coordination**

# <span id="page-23-2"></span>**5.1 General**

# <span id="page-23-3"></span>**5.1.1 Means of insulation coordination**

The design for **insulation coordination** shall be realized by means of:

- **clearances** [\(5.2\)](#page-23-7);
- **creepage distances** [\(5.3\)](#page-26-0); and
- **solid insulation** [\(5.4\)](#page-30-0)

and applies to each individual insulation under consideration.

The design requirements of **clearances** and **creepage distances** in [5.2](#page-23-7) and [5.3](#page-26-0) are minimum distances based on empirical test data as presented in [Annex A](#page-49-0) and [Annex F.](#page-66-0) During the design, production tolerances shall be taken into account.

NOTE IEC 61140 considers that isolating device requirements are applicable to **overvoltage category** III and category IV and not applicable to category I and category II.

#### <span id="page-23-4"></span>**5.1.2 Frequency above 30 kHz**

Requirements for **insulation coordination** for equipment within **low-voltage systems** with rated frequencies above 30 kHz are given in IEC 60664-4.

# <span id="page-23-5"></span>**5.1.3 Reduced distances due to coating or potting**

Requirements for **insulation coordination** for equipment within **low-voltage systems** using coating, potting or moulding for protection against **pollution**, allowing a reduction of **clearance** and **creepage distances** are given in IEC 60664-3.

# <span id="page-23-6"></span>**5.1.4 Equipment which are not connected to public low-voltage systems.**

Insulation coordination applies to equipment which is connected to the public low-voltage systems. However, it is recommended to use the same principles for all other low-voltage systems which have no connection to the public low-voltage system (IEC TR 60664-2-1).

#### <span id="page-23-7"></span>**5.2 Dimensioning of clearances**

#### <span id="page-23-8"></span>**5.2.1 General**

**Clearance** dimensions shall be selected, taking into account the following influencing technical characteristics:

- **impulse withstand voltage** (see [5.2.2.2](#page-24-2) and [5.2.2.3\)](#page-24-3);
- **temporary overvoltages**, peak voltage value (see [5.2.2.4\)](#page-24-4);
- **steady-state peak voltage** or **recurring peak voltages** (see [5.2.2.4\)](#page-24-4);
- electric field conditions (see [5.2.3.2](#page-25-3) and [5.2.3.3\)](#page-25-4);
- altitude (see [5.2.3.4\)](#page-25-2);
- **pollution degree** in the **micro-environment** (see [5.2.3.5\)](#page-25-5).

Larger **clearances** may be required due to mechanical influences such as vibration or applied forces.

See [Annex G](#page-75-0) [\(Figure](#page-75-1) G.1) for guidance on how to determine **clearance** based on the requirement of [5.2.1.](#page-23-8)

# <span id="page-24-0"></span>**5.2.2 Dimensioning criteria for clearances**

# **5.2.2.1 General**

**Clearances** shall be dimensioned to withstand the largest of the following:

- For circuits directly connected to the **mains supply**, the **rated impulse withstand voltage** determined on the basis of [5.2.2.2](#page-24-2) and [5.2.2.3.](#page-24-3)
- For a **steady-state peak voltage**, a peak value of **temporary overvoltage** (see [5.2.1\)](#page-23-8) or a **recurring peak voltage** determined on the basis of [5.2.2.4.](#page-24-4)

# <span id="page-24-2"></span>**5.2.2.2 Selection of rated impulse withstand voltage for equipment**

The **rated impulse withstand voltage** of the equipment shall be selected from [Table](#page-66-1) F.1 corresponding to the **overvoltage category** specified and to the **rated voltage** of the equipment.

NOTE 1 Equipment with a particular **rated impulse withstand voltage** and having more than one **rated voltage** can be suitable for use in different **overvoltage categories**.

NOTE 2 For consideration of the **switching overvoltage** aspect, see [4.2.2.4.](#page-15-2)

# <span id="page-24-3"></span>**5.2.2.3 Dimensioning to withstand transient overvoltages**

**Clearances** shall be dimensioned to withstand the required **impulse withstand voltage**, according to [Table](#page-67-0) F.2. For circuits directly connected to the **mains supply**, the required **impulse withstand voltage** is the **rated impulse withstand voltage** established on the basis of [5.2.2.2.](#page-24-2)

### <span id="page-24-4"></span>**5.2.2.4 Dimensioning to withstand steady-state peak voltages, temporary overvoltages or recurring peak voltages**

**Clearances** shall be dimensioned according to [Table F.8](#page-73-0) to withstand the **steady-state peak voltages**, the peak value of **temporary overvoltages** or the **recurring peak voltages**.

# <span id="page-24-1"></span>**5.2.3 Other factors involving clearances**

# **5.2.3.1 General**

The shape and arrangement of the conductive parts (electrodes) influence the homogeneity of the field (see [4.9\)](#page-23-0) and consequently the **clearance** needed to withstand a given voltage (see [Table](#page-67-0) F.2, [Table F.8](#page-73-0) and [Table](#page-49-1) A.1).

It is recommended to design for **inhomogeneous field** conditions according to [5.2.3.2](#page-25-3) (case A). If designed for **homogeneous field** conditions (case B), [5.2.3.3](#page-25-4) applies (see also [6.2.2.1\)](#page-33-3).

# <span id="page-25-3"></span>**5.2.3.2 Inhomogeneous field conditions (case A of [Table](#page-67-0) F.2)**

**Clearances,** not less than those specified in [Table](#page-67-0) F.2 for **inhomogeneous field** conditions, can be used irrespective of the shape and arrangement of the conductive parts and without verification by a voltage withstand test.

**Clearances,** through openings in enclosures of insulating material, shall not be less than those specified for **inhomogeneous field** conditions since the design is not controlled, which may have an adverse effect on the homogeneity of the electric field.

# <span id="page-25-4"></span>**5.2.3.3 Homogeneous field conditions (case B of [Table](#page-67-0) F.2)**

Values for **clearances** in [Table](#page-67-0) F.2 for case B are only applicable for **homogeneous field** conditions. They can only be used where the shape and arrangement of the conductive parts are designed to achieve an electric field having an essentially constant voltage gradient.

**Clearances** smaller than those for **inhomogeneous field** conditions require verification by a voltage withstand test (see [6.2.2.1\)](#page-33-3). For small values of **clearances**, the uniformity of the electric field can deteriorate in the presence of **pollution**, making it necessary to increase the **clearances** above the values of case B (see also [Figure](#page-52-0) A.2 and [Figure](#page-53-0) A.3).

# <span id="page-25-2"></span>**5.2.3.4 Altitude correction**

The **clearances** given in this document are valid up to 2 000 m.

For altitudes above 2 000 m, [Table](#page-50-0) A.2 should be used to determine the altitude correction factors for **clearance** correction. See also [6.2.2.1.4](#page-34-0) for the calculation procedure with respect to altitude correction for **clearances** correction. Linear interpolation is acceptable between two adjacent values of [Table](#page-50-0) A.2.

NOTE The phenomenon is nonlinear, it is the responsibility of technical committees to interpolate the value between two altitudes.

# <span id="page-25-5"></span>**5.2.3.5 Pollution degree in micro-environment**

**Clearances** shall be selected from [Table](#page-67-0) F.2 under a **pollution degree** in the **microenvironment** according to [4.5.2.](#page-19-0) The **pollution degree** does not have a strong influence on the dimensioning of **clearance**. However, it cannot be ignored for small **clearances** where pollution such as solid particles, dust and condensation could bridge the air gap.

The minimum **clearance** values in **pollution degree** 2 and **pollution degree** 3 are specified in [Table](#page-67-0) F.2.

# <span id="page-25-0"></span>**5.2.4 Dimensioning of clearances of functional insulation**

For a **clearance** of **functional insulation**, the required withstand voltage is the maximum impulse voltage or **steady-state peak voltage** (with reference to [Table F.8\)](#page-73-0) or **recurring peak voltage** (with reference to [Table F.8\)](#page-73-0) expected to occur across it, under rated conditions of the equipment, and in particular the **rated voltage** and **rated impulse withstand voltage** (refer to [Table](#page-67-0) F.2).

NOTE For distances equal to or less than 2 mm, IEC TR 63040 gives tests, research on influencing parameters to provide distances for insulation. The usage of this document is under the responsibility of the technical committees.

#### <span id="page-25-1"></span>**5.2.5 Dimensioning of clearances of basic insulation, supplementary insulation and reinforced insulation**

**Clearances** of **basic insulation** and **supplementary insulation** shall each be dimensioned as specified in [Table](#page-67-0) F.2 corresponding to:

– the **rated impulse withstand voltage**, according to [4.2.2](#page-15-0) or [5.2.2.2;](#page-24-2) or

– the **withstand voltage** requirements with respect to **transient overvoltages** generated internally in the equipment according to [4.2.2.4;](#page-15-2)

and as specified in [Table F.8](#page-73-0) corresponding to:

- the peak value of **temporary overvoltage** according to [4.2.3;](#page-16-0)
- the **recurring peak voltage** according to [4.2.4;](#page-16-1)
- the **steady-state peak voltage** according to [4.2.6.](#page-17-1)

With respect to **impulse withstand voltages**, **clearances** of **reinforced insulation** shall be dimensioned as specified in [Table](#page-67-0) F.2 corresponding to the **rated impulse withstand voltage** but one step higher in the preferred series of values in [4.2.2.1](#page-15-1) than that specified for **basic insulation**. If the **impulse withstand voltage** required for **basic insulation** according to [4.2.2.1](#page-15-1) is other than a value taken from the preferred series, **reinforced insulation** shall be dimensioned to withstand 160 % of the **impulse withstand voltage** required for **basic insulation**.

NOTE 1 In a coordinated system, **clearances** above the minimum required are unnecessary for a required **impulse withstand voltage**. However, it can be necessary, for reasons other than **insulation coordination**, to increase **clearances** (for example due to mechanical influences). In such instances, the test voltage has to remain based on the **rated impulse withstand voltage** of the equipment, otherwise undue stress of associated **solid insulation** can occur.

With respect to **steady-state peak voltages**, **recurring peak voltages** and **temporary overvoltages, clearances** of **reinforced insulation** shall be dimensioned as specified in [Table F.8](#page-73-0) to withstand 160 % of the **withstand voltage** required for **basic insulation**.

For equipment provided with **double insulation** where **basic insulation** and **supplementary insulation** cannot be tested separately, the insulation system is considered as **reinforced insulation**.

NOTE 2 When dimensioning **clearances** to accessible surfaces of insulating material, such surfaces are assumed to be covered by metal foil. Further details can be specified by technical committees.

#### <span id="page-26-0"></span>**5.3 Dimensioning of creepage distances**

#### <span id="page-26-1"></span>**5.3.1 General**

To determine the required **creepage distances**, the following influencing factors shall be taken into account:

- voltage (see [5.3.2.2\)](#page-27-2);
- **pollution degree** (see [5.3.2.3\)](#page-27-3);
- material group (see [5.3.2.4\)](#page-27-1);
- orientation and location of the **creepage distance** (see [5.3.3.2\)](#page-28-2);
- shape of insulating surface (see [5.3.3.3\)](#page-28-3);
- duration of the voltage stress (see [5.3.3.4\)](#page-28-1);
- more than one material or **pollution degree** (see [5.3.3.5\)](#page-28-4);
- floating conductive part (see  $5.3.3.6$ );
- use of ribs (see  $5.3.3.7$ );
- components mounted on printed wiring material (see [5.3.3.8\)](#page-29-4).

See [Annex H](#page-77-2) [\(Figure](#page-77-1) H.1) for guidance on how to determine creepage based on the requirement of [5.3.](#page-26-0)

NOTE The values of [Table](#page-70-0) F.5 are based upon existing empirical data and are suitable for the majority of applications. However, for **functional insulation**, values of **creepage distances** other than those of [Table](#page-70-0) F.5 can be appropriate.

# <span id="page-27-0"></span>**5.3.2 Dimensioning criteria of creepage distances**

# **5.3.2.1 General**

**Creepage distances** shall be dimensioned to withstand the long term RMS voltage stress across the considered insulation and taking into account the **pollution degree** and the material group over which the **creepage distance** is considered (see [5.3.2.2](#page-27-2) to [5.3.2.4\)](#page-27-1). Other factors considering mechanical shape, material parameters and time under voltage stress shall also be taken into account (see [5.3.3\)](#page-28-0).

# <span id="page-27-2"></span>**5.3.2.2 Determination of the voltage**

The basis for the determination of a **creepage distance** is the long-term RMS value of the voltage existing across it. This voltage is the highest value of the **steady-state working voltage** (see [4.2.5\)](#page-17-0), the **rated insulation voltage** or the **rated voltage**. For the determination of the **rated insulation voltages** [Table](#page-68-0) F.3 and [Table](#page-69-0) F.4 may be used.

**Transient overvoltages** are neglected since they will normally not influence the tracking phenomenon. However, **temporary overvoltages,** or any **overvoltage** necessary for the function of a device, should be considered if their duration and frequency of occurrence can influence tracking (see [5.3.3.4\)](#page-28-1).

For equipment having several **rated voltages** so that it may be used at different nominal voltages of the **mains supply**, the voltage selected shall be appropriate for the highest **rated voltage** of the equipment.

The highest **steady-state working voltage** which can occur in the system, equipment or internal circuits shall be used. The voltage is determined while operating at **rated voltage** and under the worst case operating conditions within the rating of the equipment. Fault conditions are not taken into account.

# <span id="page-27-3"></span>**5.3.2.3 Determination of the pollution degree**

The influence of the **pollution degree,** considering the combination of **pollution** and humidity in the **micro-environment** (see [4.5.2\)](#page-19-0), shall be taken into account when dimensioning **creepage distances** according to [Table](#page-70-0) F.5.

NOTE In an equipment, different micro-environmental conditions can exist.

#### <span id="page-27-1"></span>**5.3.2.4 Determination of the material group**

For the purposes of this document, materials are classified into four groups according to their comparative tracking index (CTI) values. These values are determined in accordance with IEC 60112 using solution A. The groups are as follows:



- $-$  material group II:  $400 ≤ CTI < 600$ ;
- material group IIIa:  $175 \leq C T I < 400$ ;
- material group IIIb:  $100 \leq C T1 < 175$ .

#### <span id="page-27-4"></span>**5.3.2.5 Relationship of creepage distance to clearance**

A **creepage distance** cannot be less than the associated **clearance** so that the shortest **creepage distance** possible is equal to the required **clearance**. However, there is no physical relationship, other than this dimensional limitation, between the minimum **clearance** in air and the minimum acceptable **creepage distance**.

**Creepage distances** less than the **clearances** required in case A of [Table](#page-67-0) F.2 may only be used under conditions of **pollution degrees** 1 and 2 when the **creepage distance** can withstand the voltage required for the associated **clearance** [\(Table](#page-67-0) F.2). For testing, see [6.2.](#page-33-0)

# <span id="page-28-0"></span>**5.3.3 Other factors involving creepage distances**

# **5.3.3.1 General**

Technical committees shall take into account other factors influencing **creepage distance,** for example the orientation and shape of the insulating surface. In case of specific influencing factors for the **creepage distances**, those factors shall be taken into account.

# <span id="page-28-2"></span>**5.3.3.2 Orientation of creepage distances**

If necessary, the manufacturer shall indicate the intended orientation of the equipment or component in order that **creepage distances** are not adversely affected by the accumulation of **pollution** for which they were not designed.

# <span id="page-28-3"></span>**5.3.3.3 Shape of insulating surface**

Shaping of insulating surfaces is effective for dimensioning of **creepage distances** under **pollution degree** 3 only. Preferably, the surface of **solid insulation** should include transverse ribs and grooves that break the continuity of the leakage path caused by **pollution**. Likewise, ribs and grooves may be used to direct any water away from insulation which is electrically stressed. Joints or grooves joining conductive parts should be avoided since they can collect **pollution** or retain water.

# <span id="page-28-1"></span>**5.3.3.4 Duration of the voltage stress**

The duration of the voltage stress influences the number of occasions when drying out can result in surface scintillations with energy high enough to entail tracking. The number of such occasions is considered to be sufficiently large to cause tracking:

- in equipment intended for continuous use but not generating sufficient heat to keep the surface of the insulation dry;
- in equipment subjected to condensation for extended periods during which it is frequently switched ON and OFF;
- on the input side of a switching device, and between its line and load terminals, that is connected directly to the **mains supply**.

The **creepage distances** shown in [Table](#page-70-0) F.5 have been determined for insulation intended to be under voltage stress during a long period of time.

Technical committees responsible for equipment in which insulation is under voltage stress for only a short time may consider allowing reduced **creepage distances.**

### <span id="page-28-4"></span>**5.3.3.5 Creepage distances where more than one material is used or more than one pollution degree occurs**

A **creepage distance** may be split into several portions of different materials and/or have different **pollution degrees** if one of the **creepage distances** is dimensioned to withstand the total voltage or if the total distance is dimensioned according to the material having the lowest comparative tracking index (CTI) and the highest **pollution degree**.

# <span id="page-28-5"></span>**5.3.3.6 Creepage distances split by floating conductive parts**

A **creepage distance** may be split into several parts, made with the same insulation material, including or separated by floating conductors as long as the sum of the distances across each individual part is equal or greater than the **creepage distance** required if the floating part did not exist. See [Figure 14.](#page-48-1)

# <span id="page-29-3"></span>**5.3.3.7 Reduction of required creepage distances by using ribs**

Required **creepage distances,** equal to or larger than 8 mm under **pollution degree** 3, may be reduced by the use of a rib. The values of these reduced **creepage distances** are those values listed in [Table](#page-70-0) F.5 in brackets (see footnote d) of [Table](#page-70-0) F.5). The rib shall have a minimum width (*W*) of 20 % and a minimum height (*H*) of 25 % of the required **creepage distance** including the rib as measured in [Figure 2.](#page-29-2)

Where more than one rib is used, the required **creepage distance** shall be divided into sections equal to the number of wanted ribs. For each section the requirements of the above paragraph shall apply. The minimum distance between the multiple ribs shall be equal to the minimum width of the rib applicable for each section, measured from the base of the rib.





# <span id="page-29-4"></span><span id="page-29-2"></span>**5.3.3.8 Creepage across component mounted on printed wiring material**

For **creepage distances** on printed wiring material only used under **pollution degree** 1 and 2, a reduced dimensioning is allowed and may be selected from [Table](#page-70-0) F.5. Attention is drawn on the possible reduction or other path of **creepage distances** due to the components.

The use of the reduced dimensioning values on printed wiring material under **pollution degree** 2 (column 3 in [Table](#page-70-0) F.5) for **creepage distances** may require an additional protection against **pollution**.

# <span id="page-29-0"></span>**5.3.4 Dimensioning of creepage distances of functional insulation**

**Creepage distances** of **functional insulation** may be dimensioned as specified in [Table](#page-70-0) F.5 corresponding to the **steady-state working voltage** across the **creepage distance** considered.

NOTE 1 The values of [Table](#page-70-0) F.5 can be used for **functional insulation** however different values of **creepage distances** can be appropriate.

NOTE 2 For distances equal to or less than 2 mm, IEC TR 63040 gives tests, research on influencing parameters to provide distances for insulation. The usage of this document is under the responsibility of the technical committees.

When the **steady-state working voltage** is used for dimensioning, it is allowed to interpolate values for intermediate voltages. When interpolating, linear interpolation shall be used and values shall be rounded to the same number of digits as the values picked up from the tables.

#### <span id="page-29-1"></span>**5.3.5 Dimensioning of creepage distances of basic insulation, supplementary insulation and reinforced insulation**

**Creepage distances** of **basic insulation** and **supplementary insulation** shall be selected from [Table](#page-70-0) F.5 for:

- the rationalized voltages given in columns 2 and 3 of [Table](#page-68-0) F.3 and columns 2, 3 and 4 of [Table](#page-69-0) F.4, corresponding to the nominal voltage of the **mains supply**;
- the **rated insulation voltage**;
- the voltage specified in [4.2.5.](#page-17-0)

When [Table](#page-70-0) F.5 is used, it is allowed to interpolate values for intermediate voltages. When interpolating, linear interpolation shall be used and values shall be rounded to the same number of digits as the values picked up from the tables.

**Creepage distances** of **double insulation** are the sum of the values of the **basic insulation** and **supplementary insulation** which make up the **double insulation** system.

NOTE 1 For **supplementary insulation**, the **pollution degree**, insulating material, mechanical stresses and environmental conditions of use can be different from those for **basic insulation**.

**Creepage distances** for **reinforced insulation** shall be twice the **creepage distance** for **basic insulation**.

NOTE 2 When dimensioning **creepage distances** to accessible surfaces of insulating material, such surfaces are assumed to be covered by metal foil. Further details can be specified by technical committees.

Comparison of the minimum **clearances** and **creepage distances** specified in this document is described in [Annex E](#page-65-0) (see [Figure](#page-65-1) E.1).

### <span id="page-30-0"></span>**5.4 Requirements for design of solid insulation**

#### <span id="page-30-1"></span>**5.4.1 General**

**Solid insulation** of **basic insulation**, **supplementary insulation** and **reinforced insulation** shall be capable of durably withstanding electrical and mechanical stresses as well as thermal and environmental influences which may occur during the intended life of the equipment.

NOTE Subclause [5.4](#page-30-0) does not provide any requirements for **solid insulation** used as **functional insulation**.

Technical committees shall consider these stresses when specifying conditions for testing.

#### <span id="page-30-2"></span>**5.4.2 Voltage stress**

**Solid insulation** shall withstand the voltage stress considering:

- **transient overvoltages** according to [5.4.3.1;](#page-30-4)
- **temporary withstand overvoltages** according to [5.4.3.2;](#page-31-0)
- **recurring peak voltages** according to [5.4.3.3;](#page-31-1)
- **steady-state working voltages** according to [5.4.3.4.](#page-31-2)

#### <span id="page-30-3"></span>**5.4.3 Withstand of voltage stresses**

#### <span id="page-30-4"></span>**5.4.3.1 Transient overvoltages**

**Basic insulation** and **supplementary insulation** shall have:

- an **impulse withstand voltage** requirement corresponding to the nominal voltage of the **mains supply** (see [4.2.2.2\)](#page-15-3), and the relevant **overvoltage category** according to [Table](#page-66-1) F.1; or
- an **impulse withstand voltage** of an internal circuit of an equipment which has been specified according to the **transient overvoltages** to be expected in the circuit (see [4.2.2.4\)](#page-15-2).

**Reinforced insulation** shall have an **impulse withstand voltage** corresponding to the **rated impulse withstand voltage** but one step higher in the preferred series of values in [4.2.2.1](#page-15-1) than that specified for **basic insulation**. If the **impulse withstand voltage** required for **basic insulation** according to [4.2.2.1](#page-15-1) is other than a value taken from the preferred series, **reinforced insulation** shall withstand 160 % of the value required for **basic insulation**.

For verification by testing, see [6.4.4.](#page-37-1)

# <span id="page-31-0"></span>**5.4.3.2 Temporary withstand overvoltages**

**Basic insulation** and **supplementary insulation** of **solid insulation** shall be designed to withstand the following **temporary withstand overvoltages**:

- short-term **temporary overvoltages** of  $U_0$  + 1 200 V with durations up to 5 s;
- $-$  long-term **temporary overvoltages** of  $U_0$  + 250 V with durations longer than 5 s;

where  $U_0$  is the nominal line-to-neutral voltage of the neutral-earthed supply system.

The performance validated can be declared by the manufacturer as a rated **temporary withstand overvoltage** value.

**Reinforced insulation** shall withstand twice the **temporary withstand overvoltages** specified for **basic insulation** except when the **partial discharge** test is used. For the **partial discharge** test, the factors given in [6.4.6.1](#page-38-2) apply.

For verification by testing**,** see [6.4.5.](#page-38-0)

NOTE 1 The values are according to IEC 60364-4-44:2007, Clause 442.

NOTE 2 The values are RMS values.

# <span id="page-31-1"></span>**5.4.3.3 Recurring peak voltages**

The maximum **recurring peak voltages** occurring on the **mains supply** can be assumed provisionally to be  $F_{\mathbf{4}} \times \sqrt{2}~U_{\mathbf{0}},$  i.e. 1,1 times the peak value at  $U_{\mathbf{0}}.$  Where  ${\sf recurring~peak}$ **voltages** are present, the **partial discharge extinction voltage** shall be at least:

- $-$  *F*<sub>1</sub>  $\times$  *F*<sub>4</sub>  $\times$   $\sqrt{2}$  *U*<sub>0</sub>, i.e. 1,32  $\sqrt{2}$  *U*<sub>0</sub> for each **basic insulation** and **supplementary insulation**, and
- $F_1 \times F_3 \times F_4 \times \sqrt{2} U_0$ , i.e. 1,65  $\sqrt{2} U_0$  for **reinforced insulation**.

NOTE  $\sqrt{2} U_0$  is in neutral-earthed systems the peak value of the line-to neutral fundamental (undistorted) voltage at the nominal voltage of the **mains supply**. The application of the multiplying factors used in this subclause is described in [Annex D.](#page-62-0)

For an explanation of factors *F*, see [6.4.6.1.](#page-38-2)

In internal circuits, the highest **recurring peak voltages** shall be evaluated in place of  $F_A \times \sqrt{2} U_0$  and **solid insulation** shall meet the corresponding requirements.

For verification by testing, see [6.4.6.](#page-38-1)

# <span id="page-31-2"></span>**5.4.3.4 Steady-state voltages**

The **steady-state working voltage** and the **steady-state peak voltage** are a long-term voltage stress applied on **solid insulation**.

In those instances where **steady-state working voltages** are non-sinusoidal with **recurring peak voltages** or **steady state peak voltages**, see [Figure 1,](#page-17-5) special consideration shall be given to possible occurrence of **partial discharges**. Similarly, where **insulation** layers may exist and where voids in moulded insulation may exist, consideration shall be given to possible occurrence of **partial discharges** with resultant degradation of **solid insulation**.

For verification by testing, see [6.4.6.](#page-38-1)

### <span id="page-32-0"></span>**5.4.4 Withstand on environmental stresses**

#### **5.4.4.1 Withstand of short-term heating stresses**

**Solid insulation** shall not be impaired by short-term heating stresses which may occur in normal and, where appropriate, abnormal use. Technical committees shall specify severity levels.

NOTE Standard severity levels are specified in IEC 60068 (all parts).

#### <span id="page-32-3"></span>**5.4.4.2 Withstand of mechanical stresses**

**Solid insulation** shall not be impaired by mechanical vibration or shock which can be expected in use. Technical committees shall specify severity levels.

NOTE Standard severity levels are specified by in IEC 60068 (all parts).

#### **5.4.4.3 Withstand of long-term heating stresses**

Thermal degradation of **solid insulation** shall not impair **insulation coordination** during the intended life of the equipment. Technical committees shall specify whether a test is necessary.

NOTE See also IEC 60085 and IEC 60216 (all parts).

#### **5.4.4.4 Withstand of the effects of humidity**

**Insulation coordination** shall be maintained under the humidity conditions as specified for the equipment (see also [6.4.3\)](#page-37-0).

#### **5.4.4.5 Other factors impacting solid insulation**

Equipment may be subjected to other stresses, for example as indicated in [4.6.2.4](#page-20-2) which may adversely affect **solid insulation**. Technical committees shall state such stresses and specify test methods.

#### <span id="page-32-1"></span>**6 Tests and measurements**

#### <span id="page-32-2"></span>**6.1 General**

The following test procedures apply to **type testing**, so that a possible deterioration of the test specimen may be tolerated. It is assumed that further use of the test specimen is not intended.

The verification procedures are specified for:

- the verification of **clearances** (see [6.2\)](#page-33-0);
- the verification of **creepage distances** (see [6.3\)](#page-35-0);
- the verification of **solid insulation** (see [6.4\)](#page-35-1);
- dielectric tests on complete equipment (see [6.5\)](#page-41-1);
- other tests (see [6.6\)](#page-42-3).

Technical committees shall consider whether **sampling tests** or **routine tests** shall be carried out in addition to **type tests** and to specify the necessary tests to be performed as sample and **routine tests** in order to ensure the quality of the insulation system during production. The tests and conditioning, as appropriate, shall be specified with test parameters adequate to detect faults without causing damage to the insulation (see [6.6.2\)](#page-42-5).

# <span id="page-33-0"></span>**6.2 Verification of clearances**

# <span id="page-33-1"></span>**6.2.1 General**

For the verification of **clearances** two cases shall be considered:

- for values according to case A of [Table](#page-67-0) F.2, verification according to [6.8](#page-43-1) is required and no further verification by voltage test is needed;
- the values smaller than the values of case A and larger than the values of case B of [Table](#page-67-0) F.2, shall be verified by the impulse voltage test according to [6.2.2.1.](#page-33-3)

The stresses for **clearances** caused by **transient overvoltages** are assessed by the impulse voltage test, which may be substituted by an AC or a DC voltage test. See [6.2.2.1.3.](#page-34-1) If the withstand against **steady-state working voltages**, **recurring peak voltages** or peak value of **temporary overvoltages** according to [5.2.2.4](#page-24-4) is decisive for the dimensioning of **clearances** and if those **clearances** are smaller than the case A values of [Table F.8,](#page-73-0) an AC test voltage according to [6.2.2.1.3.2](#page-34-2) is required.

When verifying **clearances** within equipment by an impulse voltage test, it is necessary to ensure that the specified impulse voltage appears at the **clearance** under test.

- NOTE 1 The electrical testing of **clearances** will also stress the associated **solid insulation**.
- NOTE 2 For some cases, these tests are also applied to **creepage distances**, see [5.3.2.5.](#page-27-4)
- NOTE 3 For testing complete equipment, see [6.5.](#page-41-1)

# <span id="page-33-2"></span>**6.2.2 Test voltages**

# <span id="page-33-3"></span>**6.2.2.1 Impulse voltage dielectric test**

# **6.2.2.1.1 General**

The purpose of this test is to verify that **clearances** will withstand specified **transient overvoltages**. The impulse withstand test is carried out with a voltage having a 1,2/50 µs waveform with the values specified in [Table](#page-72-0) F.6. For the waveform, IEC 61180:2016, 7.1 applies. It is intended to simulate **overvoltages** of atmospheric origin and covers **overvoltages** due to switching in the **mains supply**.

Due to the scatter of the test results of any impulse voltage test, the test shall be conducted for a minimum of three impulses of each polarity with an interval of at least 1 s between pulses.

The output impedance of the impulse generator shall not be higher than 500  $\Omega$ . When carrying out tests on equipment incorporating components across the test circuit, a much lower virtual impulse generator impedance may be specified (see IEC 61000-4-5:2014). In such cases, possible resonance effects, which can increase the peak value of the test voltage, shall be taken into account when specifying test voltage values.

Technical committees may specify alternative dielectric tests according to [6.2.2.1.3.](#page-34-1)

NOTE Values given in [Table](#page-72-0) F.6 are derived from the calculation in [6.2.2.1.4.](#page-34-0) For accuracy of information, they are given with a high level of precision. For practical application, technical committees can choose to round the values.

# **6.2.2.1.2 Selection of impulse test voltage**

If an electrical test for **insulation coordination** of equipment with respect to **clearances** is required, for **clearances** smaller than case A as specified in [Table](#page-67-0) F.2, the equipment shall be tested with the impulse test voltage corresponding to the **rated impulse withstand voltage** specified in accordance with [5.2.2.3.](#page-24-3) The impulse test voltages of [Table](#page-72-0) F.6 apply. See [6.2.2.1.4](#page-34-0) for impulse testing at altitudes different than 2 000 m.

For the test conditions, technical committees shall specify temperature and humidity values.

# <span id="page-34-1"></span>**6.2.2.1.3 Alternatives to impulse voltage dielectric tests**

### **6.2.2.1.3.1 General**

Technical committees may specify an AC or DC voltage test for particular equipment as an alternative method.

While tests with AC and DC voltages of the same peak value as the impulse test voltage specified in [Table](#page-72-0) F.6 verify the withstand capability of **clearances**, they more highly stress **solid insulation** because the voltage is applied for a longer duration. They can overload and damage certain **solid insulations**. Technical committees should therefore consider this when specifying tests with AC or DC voltages as an alternative to the impulse voltage test given in  $6.4.5.$ 

While it is possible to substitute an impulse voltage test for **clearances** by an AC or DC voltage test, it is in principle not possible to substitute an AC voltage test for **solid insulation** by an impulse voltage test. The main reasons for this are the different propagation of the impulse voltages compared to power frequency voltages, especially in complex circuits, and the dependency of the withstand characteristics of **solid insulation** on the shape and the duration of the voltage stress.

# <span id="page-34-2"></span>**6.2.2.1.3.2 Dielectric test with AC voltage**

The waveshape of the sinusoidal power frequency test voltage shall be substantially sinusoidal. This requirement is fulfilled if the ratio between the peak value and the RMS value is  $\sqrt{2}$  with a tolerance of ±3 %. The peak value shall be equal to the impulse test voltage of [Table](#page-72-0) F.6 and applied for three cycles of the AC test voltage.

# **6.2.2.1.3.3 Dielectric test with DC voltage**

The DC test voltage shall be substantially free of ripple. This requirement is fulfilled if the ratio between the peak values of the voltage and the average value is 1,0 with a tolerance of  $\pm 3$  %. The average value of the DC test voltage shall be equal to the impulse test voltage of [Table](#page-72-0) F.6 and applied three times for 10 ms in each polarity.

# <span id="page-34-0"></span>**6.2.2.1.4 Altitude correction for testing at altitudes different than 2 000 m**

According to [5.2.3.4,](#page-25-2) the **clearance** is valid for equipment used up to 2 000 m above sea level. At 2 000 m, the normal barometric pressure is 80 kPa, while at sea level the value is 101,3 kPa. See also [4.7.2.](#page-22-2)

Due to the air barometric pressure dependency, **clearances** tested according to [6.2.2.1,](#page-33-3) are tested using higher impulse test voltages at locations lower than 2 000 m. [Table](#page-72-0) F.6 gives the impulse test voltage value for verifying **clearances** at altitudes below 2 000 m.

For the purpose of testing, the factors of temperature, humidity and climatic variations of air pressure are not taken into account provided that normal laboratory conditions exist.

Normal laboratory conditions are specified in IEC 60068-1:

- temperature: 15  $^{\circ}$ C to 35  $^{\circ}$ C;
- air pressure: 86 kPa to 106 kPa at sea level;
- relative humidity: 25 % to 75 %.

The basis for the calculation of the sea level values and data for determining test values for other test locations is as follows.

The altitude correction factors given in [Table](#page-50-0) A.2 are considered in relation to the curve of [Figure](#page-51-0) A.1. The relationship is as follows:

$$
k_{\mathbf{u}} = \left(\frac{1}{k_{\mathbf{d}}}\right)^m
$$

where

- $k_{\text{u}}$  is the altitude correction for **withstand voltage** correction;
- $k_d$  is the altitude correction for **clearance** correction (see [Table](#page-74-0) F.10);
- *m* is the gradient of the relevant straight line in curve 1 in [Figure](#page-51-0) A.1 (logarithmic scales on the two co-ordinate axes) and has the value:



and *d* is the **clearance** under consideration in millimetres.

Applying altitude correction factor for **clearance** correction results in curve 1 of [Figure](#page-51-0) A.1, the voltages will be changed with five different steps at only one shifting step for distance. The mathematical formula for this operation is shown above. [Table](#page-72-0) F.6 includes this calculation as described.

In other words, each value of  $k_d$  (altitude correction factor for **clearance** correction) will produce five different values of  $k_u$  (altitude correction factor for **withstand voltage** correction) based on the five different gradients (*m*) of **withstand voltage** as a function of **clearance** (*m* having a different value for each of the five ranges of **clearance**, as laid out above).

# <span id="page-35-0"></span>**6.3 Verification of creepage distances**

**Creepage distances** shall be verified following [6.8.](#page-43-1)

For some cases, tests shall also be applied to **creepage distances**, see [5.3.2.5.](#page-27-4)

# <span id="page-35-1"></span>**6.4 Verification of solid insulation**

#### <span id="page-35-2"></span>**6.4.1 General**

The ability of **solid insulation** to withstand the voltage stresses has to be verified by a voltage test in any case. The stresses caused by **transient overvoltages** are assessed by the impulse voltage test in [6.4.4.](#page-37-1) The stresses caused by an AC **steady-state working voltage** stress can only be assessed by an AC voltage test [\(6.4.5\)](#page-38-0). The DC voltage test in [6.4.7](#page-40-0) with a test voltage equal to the peak value of the AC voltage is not fully equivalent to the AC voltage test in [6.4.5](#page-38-0) due to the different withstand characteristics of **solid insulation** for
these types of voltages. However, in case of a pure DC voltage stress, the DC voltage test in [6.4.7](#page-40-0) is appropriate.

#### <span id="page-36-1"></span>**6.4.2 Selection of tests**

**Solid insulation** that may be subjected to mechanical stresses during operation, storage, transportation or installation shall be tested with respect to vibration and mechanical shock before the dielectric testing. Technical committees may specify test methods.

The tests for **insulation coordination** are **type tests**. Technical committees shall specify which **type tests** are required for the respective stresses occurring in the equipment.

NOTE 1 Standard severity levels are specified in IEC 60068 (all parts).

For the **double insulation** test on sample:

- when the two insulations can be tested separately, two test measurements shall be performed to verify the **basic insulation** and then the **supplementary insulation**;
- when the two insulations cannot be tested separately, one test measurement shall be performed to verify the two insulations in series with the test voltage of the **reinforced insulation**.

NOTE 2 In case of a high difference in value between the two capacitances across each insulation, the constraint of this test can be more than the voltage of a **basic insulation** on one of the two insulations. It can oblige to either oversize one insulation, or to equilibrate the capacitances across each insulation.

They have the following objectives:

- a) The **impulse withstand voltage** test is to verify the capability of the **solid insulation** to withstand the **rated impulse withstand voltage** (see [5.4.3.1\)](#page-30-0);
- <span id="page-36-2"></span>b) The AC voltage test is to verify the capability of the **solid insulation** to withstand:
	- the short-term **temporary overvoltage** (see [5.4.3.2\)](#page-31-0);
	- the **recurring peak voltage** (see [5.4.3.3\)](#page-31-1);
	- the **steady-state working voltage** (see [5.4.3.4\)](#page-31-2);
	- the **steady-state peak voltage** (see [5.4.3.4\)](#page-31-2).

If the peak value of the AC test voltage is equal to or higher than the **rated impulse withstand voltage**, the impulse voltage test is covered by the AC voltage test.

**Solid insulation** has a different withstand characteristic compared to **clearances**, if the time of stress is being increased the withstand capability will be decreased significantly. Therefore, the AC voltage test, which is specified for the verification of the withstand capability of **solid insulation**, is not allowed to be replaced by an impulse voltage test.

- <span id="page-36-0"></span>c) The **partial discharge test** is to verify that no **partial discharges** are maintained in the **solid insulation** at:
	- the peak value of the long-term **temporary overvoltage** (see [5.4.3.2\)](#page-31-0);
	- the **recurring peak voltage** (see [5.4.3.3\)](#page-31-1);
	- the **steady-state working voltage** (see [5.4.3.4\)](#page-31-2);
	- the **steady-state peak voltage** (see [5.4.3.4\)](#page-31-2).
- <span id="page-36-3"></span>d) The high-frequency voltage tests for frequencies above 30 kHz is to verify the absence of failure due to dielectric heating and **partial discharge** according to [6.4.8.](#page-41-0)

NOTE 3 Information about the withstand characteristics of insulation at high frequency above 30 kHz and methods of testing is given in IEC 60664-4.

**Partial discharge** tests for **solid insulation** shall be specified if the peak value of the voltages listed under [c\)](#page-36-0) exceeds 700 V and if the average field strength is higher than 1 kV/mm. The average field strength is the peak voltage divided by the distance between two parts of different potential.

When performing tests on complete equipment, the procedure of [6.5](#page-41-1) applies.

Technical committees shall state whether the impulse test of **solid insulation** is covered by the AC voltage test of **solid insulation** or if a separate impulse voltage test of **solid insulation** is required.

#### <span id="page-37-4"></span>**6.4.3 Conditioning**

If not otherwise specified, the test shall be performed with a new test specimen. Conditioning of the specimen by temperature and humidity treatment is intended to:

- represent the most onerous normal service conditions;
- expose possible weaknesses which are not present in the new condition.

Technical committees shall specify the appropriate conditioning method from the following recommended methods:

- <span id="page-37-0"></span>a) dry heat (IEC 60068-2-2), in order to achieve a stable condition which may not exist immediately after manufacture;
- <span id="page-37-2"></span>b) change of temperature with specified rate of change (test Nb of IEC 60068-2-14:2009), in order to induce the creation of voids which could develop in storage, transportation and normal use;
- <span id="page-37-3"></span>c) thermal shock (rapid change of temperature with specified time of transfer, test Na of IEC 60068-2-14:2009), in order to induce delamination within the insulation system which may develop in storage, transportation and normal use;
- <span id="page-37-1"></span>d) damp heat, steady state (IEC 60068-2-78), in order to evaluate the effect of water absorption on the electric properties of the **solid insulation**.

For **impulse withstand voltage**, AC power frequency voltage and high frequency voltage tests, the most significant conditioning methods are those in [a\)](#page-37-0) and [d\).](#page-37-1) For **partial discharge** testing, the conditioning methods [b\)](#page-37-2) and [c\)](#page-37-3) are most relevant.

If conditioning of **solid insulation** is required, it shall be performed prior to **type testing**. The values of temperature, humidity and time shall be selected from [Table](#page-72-0) F.7. If needed, technical committees may specify higher severity values.

It may be appropriate to subject components, for example electrical parts, sub-assemblies, insulating parts and materials, to conditioning before electrical testing. When components have already been **type tested** according to [6.4.3,](#page-37-4) such conditioning is not required.

## **6.4.4 Impulse voltage test**

#### **6.4.4.1 Test method**

The methods for impulse voltage testing of [6.2.2.1](#page-33-0) apply also to **solid insulation**, except that the altitude correction factors as listed in [Table](#page-72-1) F.6 are not applicable. The test shall be conducted for five impulses of each polarity with an interval of at least 1 s between impulses. The waveshape of each impulse shall be recorded (see [6.4.4.2\)](#page-37-5).

## <span id="page-37-5"></span>**6.4.4.2 Acceptance criteria**

No **puncture** or partial breakdown of **solid insulation** shall occur during the test, but **partial discharges** are allowed. Partial breakdown will be indicated by a step in the resulting waveshape which will occur earlier in successive impulses.

NOTE **Partial discharges** in voids can lead to partial notches of extremely short durations which can be repeated in the course of an impulse.

## <span id="page-38-2"></span>**6.4.5 AC power frequency voltage test**

## <span id="page-38-1"></span>**6.4.5.1 Test method**

The waveshape of the sinusoidal power frequency test voltage shall be substantially sinusoidal. This requirement is fulfilled if the ratio between the peak value and the RMS value is  $\sqrt{2}$  with a tolerance of ±3 %. The peak value shall be equal to the highest of the voltages mentioned in [6.4.2](#page-36-1) [b\).](#page-36-2)

For **basic insulation** and **supplementary insulation**, the test voltage has the same value as the voltages mentioned in [6.4.2](#page-36-1) [b\).](#page-36-2) For **reinforced insulation**, the test voltage is twice the value used for **basic insulation**.

The AC test voltage shall be raised uniformly from 0 V to the value specified in [5.4.3.2](#page-31-0) within not more than 5 s and held at that value for at least 60 s.

In those cases where the short-term **temporary overvoltage** leads to the most stringent requirements with respect to the amplitude of the test voltage, a reduction of the duration of the test to a minimum value of 5 s can be considered by technical committees.

NOTE 1 For particular types of insulation, longer periods of testing can be required to detect weakness within the **solid insulation**.

NOTE 2 In case of testing with respect to high steady-state stresses, including high **recurring peak voltage**, technical committees can consider introducing a safety margin on the test voltage.

In some cases, the AC test voltage needs to be substituted by a DC test voltage of a value equal to the peak value of the AC voltage, however this test will be less stringent than the AC voltage test. Technical committees shall consider this situation (see [6.4.7\)](#page-40-0).

The test equipment shall comply with IEC 61180:2016.

## **6.4.5.2 Acceptance criteria**

No breakdown of **solid insulation** shall occur.

## <span id="page-38-3"></span>**6.4.6 Partial discharge test**

#### <span id="page-38-0"></span>**6.4.6.1 General**

The test equipment shall comply with IEC 61180 and the voltage waveshape as defined in 6.1.1.1 of IEC 61180:2016. The **partial discharge test voltage** shall be equal to the highest of the voltages mentioned in [6.4.2](#page-36-1) [c\)](#page-36-0) taking into account the multiplying factors  $F_1$ ,  $F_3$  and  $F_4$ as far as applicable. The resulting value is shown in [Figure 3](#page-40-1) as  $U_{\text{test voltage}}$ .

Partial discharge test methods are described in [Annex C.](#page-56-0) When performing the test, the following multiplying factors  $F_1$  to  $F_4$  apply. The use of these factors are given as an example for the **recurring peak voltage** *U*rp, see also [Annex D.](#page-62-0) The factors shall be applied to the **steady-state peak voltage** and to the peak value of long-term **temporary overvoltage** in the same way.

In the **partial discharge** test, the peak value is relevant, converting the RMS value of the **temporary overvoltage** to peak value should be done.

*F*<sup>1</sup> Basic safety factor for PD testing and dimensioning **basic insulation** and **supplementary insulation**.

The **partial discharge extinction voltage** may be influenced by environmental conditions, such as temperature. These influences are taken into account by a basic safety factor  $F_1$ of 1,2. The **partial discharge extinction voltage** for **basic insulation** or **supplementary insulation** is therefore at least 1,2  $U_{\text{rn}}$ .

 $F<sub>2</sub>$  PD hysteresis factor.

Hysteresis occurs between the **partial discharge inception voltage** and the **partial discharge extinction voltage**. Practical experience shows that *F*<sup>2</sup> is not greater than 1,25. For **basic insulation** and **supplementary insulation**, the initial value of the test voltage is therefore  $F_1 \times F_2 \times U_{\text{rp}}$ , i.e. 1,2  $\times$  1,25  $U_{\text{rp}} = 1.5$   $U_{\text{rp}}$ .

NOTE This takes into account that PD can be initiated by **transient overvoltages** exceeding **partial discharge inception voltage** and can be maintained, for example, by values of the **recurring peak voltage** exceeding **partial discharge extinction voltage**. This situation can require the combination of impulse and AC voltages for the test, which is impractical. Therefore, an AC test is performed with an initially increased voltage.

*F*<sup>3</sup> Additional safety factor for PD testing and dimensioning **reinforced insulation**.

For **reinforced insulation** a more stringent risk assessment is required. Therefore, an additional safety factor  $F_3$  = 1,25 is required. The initial value of the test voltage is  $F_1 \times F_2$ × *F*3 × *U*rp, i.e. 1,2 × 1,25 × 1,25 *U*rp = 1,875 *U*rp.

 $F_4$  Factor covering the deviation from the nominal voltage  $U_0$  of the **mains supply**.

For circuits connected to the **mains supply**, this factor takes into account the maximum deviation of the **mains supply** voltage from its nominal value. Therefore, the peak voltage at nominal voltage  $U_0$  shall be multiplied by  $F_4 = 1,1$ .

## **6.4.6.2 Verification**

The test is to verify that no **partial discharges** are maintained at the highest of the values in [6.4.2](#page-36-1) [c\):](#page-36-0)

NOTE For cases where, additionally, the actual values of **partial discharge inception voltage** and **partial discharge extinction** voltage are of interest, the measuring procedure is described in Clause [D.1.](#page-62-1)

When testing, the PD test is generally applied to components, small assemblies and small equipment.

The minimum required discharge extinction voltage shall be higher, by factor  $F_1$ , than the highest of the voltages listed in [6.4.2](#page-36-1) [c\).](#page-36-0)

According to the kind of test specimen, technical committees shall specify:

- the test circuit (see Clause [C.1\)](#page-56-1);
- the measuring equipment (see Clauses [C.3](#page-58-0) and [D.2\)](#page-62-2);
- the measuring frequency (see  $C.3.1$  and  $D.3.4$ );
- the test procedure (see  $6.4.6.3$ ).

## <span id="page-39-0"></span>**6.4.6.3 Test procedure**

The value of the test voltages and the application of the different factors are explained in [6.4.6.1.](#page-38-0) The voltage shall be raised uniformly from 0 V up to the initial test voltage. It is then kept constant for a specified time  $t_1$  not exceeding 5 s. If no **partial discharges** have occurred, the test voltage is reduced to zero after *t*1. If a **partial discharge** has occurred, the voltage is decreased to the test voltage  $U_{t}$ , which is kept constant for a specified time  $t_{2}$  until the **partial discharge** magnitude is measured, see [Figure 3.](#page-40-1)



**Figure 3 – Test voltages**

## <span id="page-40-1"></span>**6.4.6.4 Acceptance criteria**

## <span id="page-40-2"></span>**6.4.6.4.1 Specified discharge magnitude**

As the objective is to have no continuous **partial discharges** under normal service conditions, the lowest practicable value shall be specified (see [Annex D\)](#page-62-0).

NOTE 1 Except for discharges caused by corona discharges in air (e.g. in non-moulded transformers), values in excess of 10 pC are not suitable.

NOTE 2 Values as small as 2 pC are possible with currently available apparatus.

The noise level shall not be subtracted from the reading of the **partial discharge** meter.

## **6.4.6.4.2 Test result**

The **solid insulation** complies if:

- no insulation breakdown has occurred; and
- during the application of the test voltage, **partial discharges** have not occurred, or after  $t_2$ the magnitude of the discharge is not higher than specified.

## <span id="page-40-0"></span>**6.4.7 DC voltage test**

The DC voltage test with a test voltage equal to the peak value of the AC voltage is not fully equivalent to the AC voltage test due to the different withstand characteristics of **solid insulation** for these types of voltages (see [6.4.5.1\)](#page-38-1). However, in case of a pure DC voltage stress, the DC voltage test is appropriate.

The DC test voltage shall be substantially free of ripple. This requirement is fulfilled if the ratio between the peak values of the voltage and the average value is 1,0 with a tolerance of  $\pm 3$  %. The average value of the DC test voltage shall be equal to the peak value of the AC test voltage mentioned in [6.4.2](#page-36-1) [b\).](#page-36-2)

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For **basic insulation** and **supplementary insulation**, the test voltage has the same value as the voltages mentioned in [6.4.2](#page-36-1) [b\).](#page-36-2) For **reinforced insulation**, the test voltage is twice the value used for **basic insulation**.

The DC test voltage shall be raised uniformly from 0 V to the value specified in [5.4.3.2](#page-31-0) within not more than 5 s and held at that value for at least 60 s.

NOTE 1 In certain cases, the charging current due to capacitances can be too high and a longer rise time can be necessary.

Test equipment is specified in IEC 61180. It is recommended that the short-circuit output current of the generator is not less than 200 mA.

NOTE 2 For test voltages exceeding 3 kV, it is sufficient that the rated power of the test equipment is equal or greater than 600 VA.

The tripping current of the generator shall be adjusted to a tripping current of 100 mA or for test voltages above 6 kV to the highest possible value.

NOTE 3 For **routine tests**, the tripping current can be adjusted to lower levels but not less than 10 mA.

#### <span id="page-41-0"></span>**6.4.8 High-frequency voltage test**

For high-frequency voltages above 30 kHz according to [6.4.2](#page-36-1) [d\),](#page-36-3) additional or alternative AC voltage tests according to [6.4.5](#page-38-2) or **partial discharge** tests according to [6.4.6](#page-38-3) may be necessary.

NOTE Information about the withstand characteristics of insulation at high frequency above 30 kHz and methods of testing is given in IEC 60664-4.

#### <span id="page-41-1"></span>**6.5 Performing dielectric tests on complete equipment**

#### **6.5.1 General**

When performing the impulse voltage test on complete equipment, the attenuation or amplification of the test voltage shall be taken into account. It needs to be ensured that the required value of the test voltage is applied across the terminals of the equipment under test.

Surge protective devices (SPDs) shall be disconnected before dielectric testing.

NOTE If capacitors with high capacitance are parallel to the parts between which the test voltage needs to be applied, it can be difficult, or even impossible, to perform the AC voltage test because the charging current could exceed the capacity of the high voltage tester (200 mA). In the latter case, those parallel capacitors can be disconnected before testing. If this is also impossible, DC testing can be taken into consideration.

#### **6.5.2 Parts to be tested**

The test voltage shall be applied between parts of the equipment which are electrically separate from each other.

Examples of such parts include:

- live parts;
- separate circuits;
- earthed circuits;
- accessible surfaces.

Non-conductive parts of accessible surfaces shall be covered with metal foil. If a complete covering of large enclosures with metal foil is not practicable, a partial covering is sufficient if applied to those parts which provide protection against electric shock.

## **6.5.3 Preparation of equipment circuits**

For the test, each circuit of the equipment shall be prepared as follows:

- external terminals of the circuit, if any, shall be connected together;
- switchgear and controlgear within equipment shall be in the closed position or bypassed;
- the terminals of voltage blocking components (such as rectifier diodes) shall be connected together:
- components such as RFI filters shall be included in the impulse test but it may be necessary to disconnect them during AC tests.

For the test, to include some specific components as follows:

- voltage sensitive components within any circuit of the equipment, which do not bridge **basic insulation**, **supplementary insulation**, or **reinforced insulation**, may be bypassed by shorting the terminals;
- pre-tested plug-in printed circuit boards and pre-tested modules with multipoint connectors may be withdrawn, disconnected or replaced by dummy samples to ensure that the test voltage is propagated inside the equipment to the extent necessary for the insulation tests.

#### **6.5.4 Test voltage values**

Circuits connected to the **mains supply** are tested according to [6.2](#page-33-1) and [6.4.](#page-35-0)

The test voltage between two circuits of the equipment shall have the value corresponding to the highest voltage that actually can occur between these circuits.

#### **6.5.5 Test criteria**

There shall be no disruptive discharge (**sparkover**, **flashover** or **puncture**) during the test. **Partial discharges** in **clearances** which do not result in breakdown are disregarded, unless otherwise specified by the technical committees.

NOTE An oscilloscope can be used to observe the impulse voltage in order to detect disruptive discharge.

#### **6.6 Other tests**

#### **6.6.1 Test for purposes other than insulation coordination**

Technical committees specifying electrical tests for purposes other than verification of **insulation coordination** shall not specify test voltages higher than those required for **insulation coordination**.

#### **6.6.2 Sampling and routine tests**

**Sampling tests** and **routine tests** are intended to ensure production quality. It is the responsibility of the relevant technical committee, and in particular of the manufacturer, to specify these tests. They shall be carried out with the waveforms and voltage levels such that faults are detected without causing damage to the equipment (**solid insulation** or components).

Technical committees specifying **sampling tests** and **routine tests** shall in no case specify test voltages higher than those required for **type testing**.

## **6.6.3 Measurement accuracy of test parameters**

All important test parameters shall be measured with high accuracy in order to provide well defined and comparable test results. For the purpose of harmonization, the accuracy of measurement of the measuring devices used for the following test parameters is given in this document as follows:



NOTE The given accuracy refers to that of the humidity measuring device. It does not include the humidity uniformity within the chamber and/or the influence of the test sample on the humidity uniformity. The humidity in the chamber is measured only at one place before testing the sample.

f) **partial discharge** magnitude: ±10 % or ±1 pC (the greater values applies);



#### **6.7 Measurement of the attenuation of the transient overvoltages**

The proposed measurement (see [4.2.2.5\)](#page-16-0) of the attenuation of transients is only possible by use of a suitable impulse generator with very low output impedance.

Such measurement may be performed by the use of the "1,2/50 µs combination wave generator" according to IEC 61000-4-5 with an effective output impedance of 2  $\Omega$ .

NOTE The output impedance of 2 Ω is the worst case, see also IEC 61000-4-5:2014, Clause C.1 to the different source impedance to each coupling mode.

#### **6.8 Measurement of clearances and creepage distances**

**Clearances** and **creepage distances** are verified as a minimum by:

- physical measurement or,
- inspection of design board, drawings or,
- (CAD 2D or 3D) computer-aided design.

The methods of measuring **clearances** and **creepage distances** are indicated in [Figure 4](#page-44-0) to [Figure 14.](#page-48-0) These cases do not differentiate between gaps and grooves or between types of insulation. Also, where the example is shown to form an angle, these cases may apply to any angle.

The following assumptions are made:

- Where the distance across a groove is less than the specified width *X* (see [Table 1\)](#page-44-1), the **creepage distance** is measured directly across the groove and do not take into account the contour of the groove (see [Figure 4\)](#page-44-0).
- where the distance across a groove is equal to or larger than the specified width  $X$  (see [Table 1\)](#page-44-1), the **creepage distance** is measured along the contours of the groove (see [Figure 5\)](#page-45-0);
- any recess is assumed to be bridged with an insulating link having a length equal to the specified width  $X$  and being placed in the most unfavourable position (see [Figure 6\)](#page-45-1);
- **clearances** and **creepage distances** measured between parts which can assume different positions in relation to each other, are measured when these parts are in their most unfavourable position.

The dimension *X*, specified in the following examples, has a minimum value depending on the **pollution degree** as given in [Table 1.](#page-44-1)



## **Table 1 – Dimensioning of grooves**

<span id="page-44-1"></span>If the associated **clearance** requirement is less than 3 mm, the minimum dimension *X* may be reduced to one-third of the associated **clearance**.



Condition: Path under consideration includes a parallel- or converging-sided groove of any depth with a width less than *X* mm.

Rule: **Clearance** and **creepage distance** are measured directly across the groove as shown.

<span id="page-44-0"></span>

**Creepage distance** 

**Figure 4 – Across the groove**





**Figure 5 – Contour of the groove**

<span id="page-45-0"></span>

<span id="page-45-1"></span>

**Clearance Clearance Creepage distance** 





Condition: Path under consideration includes an uncemented joint with grooves less than *X* mm wide on each side.





**Figure 8 – Uncemented joint with grooves less than** *X*



Condition: Path under consideration includes an uncemented joint with grooves equal to or more than *X* mm wide on each side.

Rule: **Clearance** is the "line of sight" distance. **Creepage** path follows the contour of the grooves.

**Clearance Clearance Creepage distance** 

**Figure 9 – Uncemented joint with grooves equal to or more than** *X* 



Condition: Path under consideration includes an uncemented joint with a groove on one side less than *X* mm wide and the groove on the other side equal to or more than *X* mm wide.

Rule: **Clearance** and **creepage** paths area as shown.



**Figure 10 – Uncemented joint with a groove on one side less than** *X*



Condition: **Creepage distance** through the uncemented joint is less than the **creepage distance** over the barrier but more than the **clearance** over the top of the barrier.



```
Clearance Clearance Creepage distance
```




Gap between head of screw and wall of recess wide enough to be taken into account.







Gap between head of screw and wall of recess too narrow to be taken into account.

Measurement of **creepage distance** is from head of screw to wall when the distance is equal to *X* mm.

- -

**Clearance Clearance Clearance Creepage distance** 





#### <span id="page-48-0"></span>**Figure 14 – Creepage distance and clearance with conductive floating part**

# **Annex A**

# (informative)

# **Basic data on withstand characteristics of clearances**

# <span id="page-49-0"></span>**Table A.1 – Withstand voltages for an altitude of 2 000 m above sea level** *(1 of 2)*



	Case A	Case B <b>Homogeneous field</b>						
AC.		Impulse $(1,2/50 \text{ }\mu\text{s})$	AC. (50/60 Hz)	AC. (50/60 Hz) and impulse $(1,2/50 \,\mu s)$				
$U$ RMS	Ĥ	Û	$U$ RMS	Ĥ				
35.4	$50.0+$	65,0	148.5	$210.0+$				
SOURCE The information for clearances from 0.001 mm to 0.008 mm, is issued from the document "Electrical" breakdown experiments in air for micrometer gaps under various pressures" from P. Hartherz, K. Ben Yahia, L. Müller, R. Pfendtner and W. Pfeiffer (see bibliography). More details can be found in the thesis of P. Hartherz "Anwendung der Teilentladungsmeßtechnik zur								
		(50/60 Hz)	Inhomogeneous field	Fehleranalyse in festen Isolierungen unter periodischer Impulsspannungsbelastung" (see bibliography).				

**Table A.1** *(2 of 2)*

For simplification, the statistical measured values according to [Table](#page-49-0) A.1 above are replaced by straight lines between the values marked "+" in a double logarithmic diagram taking into account the correction factors from 0 m to 2 000 m altitude. The intermediate values are taken from that diagram (see [Figure](#page-51-0) A.1) so that they enclose the measured values with a small safety margin. The values of *U* RMS are found by dividing the values of  $\hat{U}$  by  $\sqrt{2}$ .

<b>Altitude</b>	<b>Normal</b> barometric pressure	Multiplication factor $k_d$ for clearances			
m	kPa				
2 0 0 0	80,0	1,00			
3 0 0 0	70,0	1,14			
4 0 0 0	62,0	1,29			
5 0 0 0	54,0	1,48			
6 0 0 0	47,0	1,70			
7 000	41,0	1,95			
8 0 0 0	35,5	2,25			
9 0 0 0	30,5	2,62			
10 000	26,5	3,02			
15 000	12,0	6,67			
20 000	5, 5	14,5			

**Table A.2 – Altitude correction factors for clearance correction**

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- 2 case A; *Û* 1,2/50 µs
- <span id="page-51-0"></span>3 case A; *Û* 50/60 Hz

# **Figure A.1 – Withstand voltage at 2 000 m above sea level**



- 1 *Û* 1,2/50 µs according to Pfeiffer, W., ETZ-B, 1976 (see bibliography)
- 2 Low limits for  $\hat{U}$  1,2/50 µs
- 3 *Û* 50 Hz according to Hermstein, W., ETZ-A, 1969 (see bibliography)
- 4 Low limits for *Û* 50 Hz

**Figure A.2 – Experimental data measured at approximately sea level and their low limits for inhomogeneous field**



- 1 *Û* 1,2/50 µs according to Pfeiffer, W., ETZ-B, 1976 (see bibliography)
- 2 *Û* 50 Hz according to Dakin, T. et al., Electra, 1974 (see bibliography)
- 3 Low limits for *Û* 1,2/50 µs and *Û* 50 Hz

## **Figure A.3 – Experimental data measured at approximately sea level and their low limits for homogeneous field**

# **Annex B**

## (informative)

# <span id="page-54-0"></span>**Nominal voltages of mains supply for different modes of overvoltage control**

## **Table B.1 – Inherent control or equivalent protective control**



<sup>a</sup> These columns are taken from [Table](#page-66-0) F.1 in which the **rated impulse withstand voltage** values are specified.

<sup>b</sup> Practice in the United States of America and in Canada.

c Practice in Japan.

<sup>d</sup> Only applicable for direct current.

#### **Table B.2 – Cases where protective control is necessary and control is provided by surge protective device having a ratio of voltage protection level to rated voltage not smaller than that specified in IEC 61643 (all parts)**



<sup>a</sup> These columns are taken from [Table](#page-66-0) F.1 in which the **rated impulse withstand voltage** values are specified.

<sup>b</sup> Practice in the United States of America and in Canada.

<sup>c</sup> Practice in Japan.

<sup>d</sup> Only applicable for direct current.

# **Annex C**

## (normative)

## **Partial discharge test methods**

## <span id="page-56-1"></span><span id="page-56-0"></span>**C.1 Test circuits**

#### **C.1.1 General**

Test circuits shall perform as described in IEC 60270. The following circuits given in [Annex C](#page-56-0) meet those requirements and are given as examples.

NOTE 1 In the majority of cases, testing equipment designed in accordance with the examples given in Annex C will be sufficient. In special cases, for example in presence of extremely high ambient noise, it can be necessary to refer to IEC 60270.

NOTE 2 For an explanation of the basic operation, see Clause [D.2.](#page-62-2)

## <span id="page-56-3"></span>**C.1.2 Test circuit for earthed test specimen [\(Figure](#page-56-2) C.1)**



**IEC** 

**Key**

- $U_t$  test voltage
- Z filter
- $C<sub>a</sub>$  test specimen (usually it can be regarded as a capacitance)
- $C_k$  coupling capacitor
- <span id="page-56-2"></span>Z<sub>m</sub> measuring impedance

#### **Figure C.1 – Earthed test specimen**

## <span id="page-57-1"></span>**C.1.3 Test circuit for unearthed test specimen [\(Figure](#page-57-0) C.2)**



**Key**

- $U_t$  test voltage
- Z filter
- $C_{\rm a}$  test specimen (usually it can be regarded as a capacitance)
- *C*<sup>k</sup> coupling capacitor
- <span id="page-57-0"></span>*Z*<sup>m</sup> measuring impedance

## **Figure C.2 – Unearthed test specimen**

## **C.1.4 Selection criteria**

Basically, both circuits are equivalent. However, the stray capacitances of the test specimen have a different influence upon sensitivity. The earth capacitance of the high-voltage terminal of the test specimen tends to reduce the sensitivity of the circuit according to [C.1.2](#page-56-3) and tends to increase the sensitivity of the circuit according to [C.1.3](#page-57-1) which therefore should be preferred.

## <span id="page-57-2"></span>**C.1.5 Measuring impedance**

The measuring impedance shall provide a negligibly low-voltage drop at test frequency. The impedance for the measuring frequency shall be selected in order to provide a reasonable sensitivity, see Clause [D.2.](#page-62-2)

If voltage limiting components are used, they shall not be effective within the measuring range.

## **C.1.6 Coupling capacitor**  $C_k$

This capacitor shall be of low inductance type with a resonant frequency in excess of 3 $f<sub>2</sub>$ (see Clause [C.3\)](#page-58-0). It shall be free of **partial discharges** up to the highest test voltage used.

## **C.1.7 Filter**

The use of a filter is not mandatory. If used, its impedance shall be high for the measuring frequency.

## **C.2 Test parameters**

## **C.2.1 General**

Technical committees shall specify:

- $-$  the frequency  $f_t$  of the test voltage [\(C.2.2\)](#page-58-2);
- the **specified discharge magnitude** [\(6.4.6.4.1\)](#page-40-2);

– the climatic conditions for the PD test [\(C.2.3\)](#page-58-3).

NOTE It can be necessary to have different specifications for the **type test** and the **routine test.**

#### <span id="page-58-2"></span>**C.2.2 Requirements for the test voltage**

Normally AC voltages are used. The total harmonic distortion shall be less than 3 %.

NOTE 1 Low distortion of the sine wave allows the use of standard voltmeters and the calculation of the peak value from the RMS reading. In the case of higher distortion, peak voltmeters can be used.

Tests are normally made at power frequency. If other frequencies are present in the equipment, technical committees shall consider the possible effect of frequency on discharge magnitude.

NOTE 2 PD testing with DC voltage can be unsuitable because of the difficulty of achieving an **environment** which is completely free of electrical noise. In addition it can be noted that the voltage distribution is greatly different for alternating current and direct current.

#### <span id="page-58-3"></span>**C.2.3 Climatic conditions**

It is recommended to perform the test at room temperature and average humidity (23 °C, 50 % RH, see IEC 60068-1:2013, 4.3).

#### <span id="page-58-0"></span>**C.3 Requirements for measuring instruments**

#### <span id="page-58-1"></span>**C.3.1 General**

Both wideband and narrowband charge measuring instruments may be used (see [C.3.3\)](#page-59-0). Radio interference voltmeters may only be used according to the precautions given in [C.3.2.](#page-58-4)

The lower limit of the measuring frequency is determined by the frequency  $f_t$  of the test voltage and the frequency characteristic of the measuring impedance  $Z_m$  (see [C.1.5\)](#page-57-2). It should not be lower than 10  $f_{\mathfrak{k}}.$ 

The upper limit of the measuring frequency is determined by the shape of the PD pulses and the frequency response of the test circuit. It does not need to be higher than 2 MHz. For narrowband PD meters the measuring frequency shall be selected with regard to narrowband noise sources (see [D.3.4\)](#page-63-0).

NOTE Narrowband PD meters are suitable.

#### <span id="page-58-4"></span>**C.3.2 Classification of PD meters**

The current through the measuring impedance  $Z_m$  is integrated to provide a reading proportional to  $q_m$  (see [Figure](#page-62-3) D.1).

The integration can be affected by the measuring impedance. In this case, it shall represent a capacitance for all frequencies above the lower limit of the measuring frequency. The voltage across the capacitance, which is proportional to  $q_m$ , is amplified by a pulse amplifier. Periodic discharging shall also be provided.

If the measuring impedance is resistive for all frequencies above the lower limit of the measuring frequency, the integration shall be done within the pulse amplifier.

Single pulses shall be measured and the pulse with the maximum amplitude shall be evaluated. In order to limit errors due to pulse overlap, the pulse resolution time shall be less than  $100$   $\mu$ s.

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Radio interference meters are narrowband peak voltage meters. They are used to measure interference of radio signals. They incorporate a special filter circuit which creates dependency of the reading on the **pulse repetition rate** according to the subjective effect of noise to the human ear.

For measuring **partial discharges**, radio interference meters may only be used if the filter circuit is disconnected. Also, a suitable measuring impedance is required.

#### <span id="page-59-0"></span>**C.3.3 Bandwidth of the test circuit**

Usually, the PD meter limits the bandwidth of the test circuit. PD meters are classified according to their bandwidth as wideband or narrowband.

- a) The lower and the upper cut-off frequencies  $f_1$  and  $f_2$  are those where the frequency response has dropped by 3 dB of the constant value in the case of a wideband meter and by 6 dB from the peak value in the case of a narrowband meter.
- b) For narrowband meters, the measuring frequency  $f_0$  is identical with the resonance peak in the frequency response.
- c) The bandwidth ∆*f* is:

$$
\Delta f = f_2 - f_1
$$

For wideband meters, ∆*f* is in the same order of magnitude as *f*<sub>2</sub>. For narrowband meters, ∆*f* is much less than *f*<sub>0</sub>.

## **C.4 Calibration**

#### <span id="page-59-1"></span>**C.4.1 Calibration of discharge magnitude before the noise level measurement**

The calibration of the test circuit [\(Figure](#page-60-0) C.3 or [Figure](#page-60-1) C.4) shall be carried out at the **specified discharge magnitude** replacing the test specimen  $C_a$  by a capacitor  $C_x$  which exhibits no **partial discharge**. The impedance of the capacitor  $C_x$  shall be similar to that of the test specimen  $C_{\alpha}$ .

The transformers shall be adjusted according to the specified **partial discharge test voltage** but not energized and their primary windings shall be short-circuited. The **specified discharge magnitude** shall be applied to the terminals of the capacitor by means of the calibration pulse generator. The indication of the discharge magnitude on the discharge detector shall be adjusted to correspond with the calibration signal.



- $U_t$  test voltage
- Z filter
- $C_0$  capacitance of the calibration impulse generator
- $C_{\mathsf{a}}$  or  $C_{\mathsf{x}}$  test specimen (usually it can be regarded as a capacitance)
- *C*<sub>k</sub> coupling capacitor
- <span id="page-60-0"></span>*Z*<sup>m</sup> measuring impedance





#### **Key**

 $U_t$  test voltage

Z filter

 $C_0$  capacitance of the calibration impulse generator

 $C_{\mathbf{a}}$  or  $C_{\mathbf{x}}$  test specimen (usually it can be regarded as a capacitance)

*C*<sub>k</sub> coupling capacitor

<span id="page-60-1"></span>*Z*<sup>m</sup> measuring impedance

## **Figure C.4 – Calibration for unearthed test specimen**

## **C.4.2 Verification of the noise level**

With the arrangement used in [C.4.1,](#page-59-1) the PD test voltage shall be raised up to the highest test voltage. The maximum noise level shall be less than 50 % of the **specified discharge magnitude**. Otherwise measures according to Clause [D.3](#page-63-1) are required.

## **C.4.3 Calibration for the PD test**

With the test specimen in circuit, the procedure of [C.4.1](#page-59-1) shall be repeated.

Changes in the test circuit or test specimen require recalibration. In the case of many similar test specimens, occasional recalibration may be sufficient if:

- the impedance of the coupling capacitor is less than 1/10 of that of the test specimen; or
- the impedance of the test specimen does not deviate from the value during calibration by more than ±10 %.

NOTE When specifying time intervals for recalibration, technical committees can bear in mind that, in case of insufficient sensitivity at the PD meter, potentially harmful discharges cannot be detected.

#### **C.4.4 Calibration pulse generator**

For the calibration pulse generator, see IEC 60270 that explains the test method and the characteristics to be checked.

# **Annex D**

(informative)

# **Additional information on partial discharge test methods**

## <span id="page-62-1"></span><span id="page-62-0"></span>**D.1 Measurement of partial discharge (PD), PD inception and extinction voltage**

The test voltage is increased from a value below the **partial discharge inception voltage** until **partial discharges** occur (PD inception voltage). After further increase of the test voltage by 10 %, the voltage is decreased until PD is smaller than the **specified discharge magnitude** (PD extinction voltage). Thereby the insulation test voltage specified for the test specimen may not be exceeded.

NOTE It can occur that the **partial discharge extinction voltage** is influenced by the time of the voltage stress with values exceeding the **partial discharge inception voltage**. During successive measurements, both **partial discharge inception voltage** and **partial discharge extinction voltage** can be influenced.

This procedure is appropriate for investigation measurements.

## <span id="page-62-2"></span>**D.2 Description of PD test circuits [\(Figure](#page-62-3) D.1)**

Each circuit consists of the following devices:

- the test specimen  $C_a$  (in special cases it may also be an impedance  $Z_a$ );
- the coupling capacitor  $C_{\mathbf{k}}$ ;
- the measuring circuit consisting of measuring impedance  $Z<sub>m</sub>$ , the connecting cable and the PD meter;
- optionally a filter *Z* to reduce charge being bypassed by the test voltage source.



#### **Key**



## <span id="page-62-3"></span>**Figure D.1 – Partial discharge test circuits**

The direct measurement of the **apparent charge** *q* would require a short-circuit at the terminals of the test specimen for the measuring frequency. This condition can be approximated as follows:

- $C_k$  >  $(C_a + C_e);$
- high impedance *Z*;
- low measuring impedance  $Z_{\rm m}$ .

Otherwise significant charge losses  $q_{v2}$  and  $q_{v3}$  may occur. These charge losses are taken into account by the calibration but they will limit the sensitivity. The situation is aggravated if the test specimen has a high capacitance.

## <span id="page-63-1"></span>**D.3 Precautions for reduction of noise**

## **D.3.1 General**

The results of PD measurements may be greatly influenced by noise. Such noise may be introduced by conductive coupling or by electromagnetic interference. In unscreened industrial test sites, single charge pulses as high as 100 pC may occur due to noise. Even under favourable conditions, not less than 20 pC can be expected.

A noise level as low as 1 pC may be achieved, but this will require screening of the test circuit, careful earthing measures and filtering of the **mains supply** input.

Basically, there are two different kinds of noise sources as described in [D.3.2](#page-63-2) and [D.3.3.](#page-63-3)

## <span id="page-63-2"></span>**D.3.2 Sources in the non-energized test circuit**

These are caused for instance by switching in adjacent circuits. In case of conductive coupling they only occur if connection to the **mains supply** is provided. In case of electromagnetic coupling they also occur if the **mains supply** is switched off (including the protective conductor).

## <span id="page-63-3"></span>**D.3.3 Sources in the energized test circuit**

Usually, noise increases with the test voltage and is caused by **partial discharges** outside the test specimen. PD may occur in the test transformer, the high-voltage connecting leads, bushings and points of poor contact. Harmonics of the test voltage may also contribute to the noise level.

## <span id="page-63-0"></span>**D.3.4 Measures for reduction of noise**

Noise caused by conductive coupling can be reduced by use of line filters in the central feeding of the test circuit. No earth loops should be present.

Electromagnetic interference, for instance by radio signals, can be excluded in a simple manner by variation of the measuring frequency  $f_0$  for narrowband PD meters. For wideband PD meters, band-stop-filters may be required, wideband signals can only be suppressed by screening. The highest efficiency is provided by a fully enclosed screen with high electrical conductivity.

## **D.4 Application of multiplying factors for test voltages**

## **D.4.1 General**

The values of the multiplying factors defined in [6.4.6](#page-38-3) and used in [5.4.3.3](#page-31-1) and [6.4.6](#page-38-3) are calculated as follows.

NOTE These examples are given for the **recurring peak voltage** *U*rp. The factors similarly apply to the **steady-state peak voltage** and to the long-term peak value of **temporary overvoltage**.

#### **D.4.2 Example 1 (circuit connected to mains supply)**

## **D.4.2.1 Maximum recurring peak voltage**  $U_{\text{rn}}$

$$
U_{\rm rp} = \sqrt{2} U_0 \times F_4 = 1.1 \sqrt{2} U_0
$$

## **D.4.2.2 Partial discharge extinction voltage** *U***extinction (basic insulation)**

 $U_{\text{extinction}} = \sqrt{2} U_0 \times F_4 \times F_1$ 

 $U_{\text{extinction}} = \sqrt{2} U_0 \times 1.1 \times 1.2 = 1.32 \sqrt{2} U_0$ 

**D.4.2.3 Initial value of the PD test voltage** *U***<sup>1</sup> (basic insulation)**

$$
U_1 = \sqrt{2} U_0 \times F_4 \times F_1 \times F_2
$$

$$
U_1 = \sqrt{2} U_0 \times 1,32 \times 1,25 = 1,65\sqrt{2} U_0
$$

**D.4.3 Example 2 (internal circuit with maximum recurring peak voltage**  $U_{\text{rp}}$ **)** 

**D.4.3.1 Partial discharge extinction voltage** *U***extinction (basic insulation)**

$$
U_{\text{extinction}} = U_{\text{rp}} \times F_1 = U_{\text{rp}} \times 1.2
$$

**D.4.3.2 Initial value of the PD test voltage** *U***<sup>1</sup> (basic insulation)**

 $U_1 = U_{\text{rp}} \times F_1 \times F_2 = U_{\text{rp}} \times 1.5$ 

## **Annex E**





# **Comparison of creepage distances specified in [Table](#page-70-0) F.5 and clearances in [Table](#page-49-0) A.1**

 $IEC$ 

#### **Key**

PD **pollution degree**

MG material group

PWM printed wiring material



# **Annex F**

## (normative)

## **Tables**

#### **Table F.1 – Rated impulse withstand voltage for equipment energized directly from the mains supply**

<span id="page-66-0"></span>

<sup>a</sup> See [Annex B](#page-54-0) for application to existing different low-voltage **mains supply** and their nominal voltages.

<sup>b</sup> Equipment with these **rated impulse withstand voltages** can be used in installations in accordance with IEC 60364-4-44.

<sup>c</sup> The / mark indicates a four-wire three-phase distribution system. The lower value is the voltage line-toneutral, while the higher value is the voltage line-to-line. Where only one value is indicated, it refers to threewire, three-phase systems and specifies the value line-to-line.

<sup>d</sup> See [4.3](#page-17-0) for an explanation of the **overvoltage categories**.

<sup>e</sup> Nominal voltages for single-phase systems in Japan are 100 V or 100 V to 200 V. However, the value of the **rated impulse withstand voltage** for the voltages is determined from columns applicable to the voltage line to neutral of 150 V (See [Annex B\)](#page-54-0).

For DC values only.

<span id="page-67-0"></span>

#### **Table F.2 – Clearances to withstand transient overvoltages**

a This voltage is:

– for **functional insulation**, for **basic insulation** directly exposed to or significantly influenced by **transient overvoltages** from the **mains supply** (see [5.2.2.2,](#page-24-0) [5.2.2.3](#page-24-1) and [5.2.4\)](#page-25-0), the **rated impulse withstand voltage** of the equipment,

– for other **basic insulation** (see [5.2.5\)](#page-25-1), the highest impulse voltage that can occur in the circuit.

For **reinforced insulation**, see [5.2.5.](#page-25-1)

b Preferred values as specified in [4.2.2.1.](#page-15-0)

<sup>c</sup> For printed wiring material, the values for **pollution degree** 1 apply except that the value shall not be less than 0,04 mm, as specified in [Table F.5.](#page-70-0) A protection by means of a solder resist of high quality is the minimum requirement to allow this **clearance** reduction.

<sup>d</sup> The minimum **clearances** given for **pollution degrees** 2 and 3 are based on the reduced withstand characteristics of the associated **creepage distance** under humidity conditions.

<sup>e</sup> The dimensions for **pollution degree** 4 are as specified for **pollution degree** 3, except that the minimum **clearance** is 1,6 mm.

<span id="page-68-0"></span>

## **Table F.3 – Single-phase three-wire or two-wire AC or DC systems**

Line-to-earth insulation level for unearthed or impedance-earthed systems equals that for line-to-line because the operating voltage to earth of any line can, in practice, approach full line-to-line voltage. This is because the actual voltage to earth is determined by the insulation resistance and capacitive reactance of each line to earth; thus, low (but acceptable) insulation resistance of one line can in effect earth it and raise the other two to full line-to-line voltage to earth.

<sup>b</sup> For relationship to **rated voltage** see [5.3.2.2.](#page-27-0)

<sup>c</sup> These values correspond to the values given in [Table](#page-66-0) F.1.

<sup>d</sup> For DC values only.



## **Table F.4 – Three-phase four-wire or three-wire AC systems**

Line-to-earth insulation level for unearthed or impedance-earthed systems equals that for line-to-line because the operating voltage to earth of any line can, in practice, approach full line-to-line voltage. This is because the actual voltage to earth is determined by the insulation resistance and capacitive reactance of each line to earth; thus, low (but acceptable) insulation resistance of one line can in effect earth it and raise the other two to full line-to-line voltage to earth.

<sup>b</sup> For equipment for use on both three-phase four-wire and three-phase three-wire supplies, earthed and unearthed, use the values for three-wire systems only.

<sup>c</sup> For relationship to **rated voltage** see [5.3.2.2.](#page-27-0)

<sup>d</sup> These values correspond to the values given in [Table](#page-66-0) F.1.

<span id="page-70-0"></span>

# **Table F.5 – Creepage distances to avoid failure due to tracking** *(1 of 2)*

	Minimum creepage distances								
Voltage RMS $a, e$	<b>Printed wiring</b> material								
	<b>Pollution degree</b>								
	1	2 <sup>f</sup>	1	$\mathbf{2}$			3		
	All All material material groups, except groups <b>IIIb</b>	All material	<b>Material</b> group	<b>Material</b> group	<b>Material</b> group	<b>Material</b> group	<b>Material</b> group	<b>Material</b> group	
			groups	т	$\mathbf{H}$	$\mathbf{III}$	п	$\mathbf{H}$	III <sub>p</sub>
V	mm	mm	mm	mm	mm	mm	mm	mm	mm
4 0 0 0			16,0	20,0	28,0	40,0	50.0 $(40,0)^d$	56,0 $(44,8)^d$	63.0 $(50, 4)$ <sup>d</sup>
5 0 0 0			20,0	25,0	36,0	50,0	63.0 $(50,4)$ <sup>d</sup>	71.0 $(56,8)^d$	80.0 $(64,0)^d$
6 300			25,0	32,0	45,0	63,0	80,0 $(64,0)^d$	90,0 $(72,0)^d$	100,0 $(80,0)^d$
8 0 0 0			32,0	40,0	56,0	80,0	100,0 $(80,0)^d$	110,0 $(88,0)^d$	125,0 $(100,0)^d$
10 000			40,0	50.0	71,0	100.0	125,0 $(100, 0)^{d}$	140.0 $(112,0)^d$	160,0 $(128,0)^{d}$
12 500			$50,0^{\circ}$	63,0 $C$	90,0 °	125,0 $C$			
16 000			63,0 $C$	$80,0^{\circ}$	110,0 $C$	160.0 $\degree$			
20 000			$80,0$ <sup>c</sup>	100,0 $C$	140,0 $C$	$200,0$ <sup>c</sup>			
25 000			100,0 $\degree$	125,0 $C$	180,0 $C$	$250,0^{\circ}$			
32 000			125,0 $C$	160,0 $C$	$220,0^{\circ}$	320,0 $C$			
40 000			160.0 $C$	200.0 °	$280,0^{\circ}$	400,0 <sup>c</sup>			
50 000			$200,0$ <sup>c</sup>	250.0 °	360,0°	500.0 $C$			
63 000			$250,0^{\circ}$	320,0 $C$	450,0 $\degree$	600.0 $\degree$			

**Table F.5** *(2 of 2)* 

NOTE The high precision for **creepage distances** given in this table does not mean that the uncertainty of measurement has to be in the same order of magnitude.

a This voltage is for:

- **functional insulation**, the **steady-state working voltage** (see [5.3.4\)](#page-29-0),
- **basic insulation** and **supplementary insulation** of the circuit energized directly from the **mains supply** (see [5.3.5\)](#page-29-1), the voltage rationalized through [Table F.3](#page-68-0) or [Table F.5,](#page-70-0) based on the **rated voltage** of the equipment, or the **rated insulation voltage**,
- **basic insulation** and **supplementary insulation** of a system, equipment and internal circuits not energized directly from the **mains supply** (see [5.3.5\)](#page-29-1), the highest RMS voltage which can occur in the system, equipment or internal circuit when supplied at **rated voltage** and under the most onerous combination of conditions of operation within equipment rating.
- <sup>b</sup> Material group IIIb is not recommended for application in **pollution degree** 3 above 630 V.
- Provisional data based on extrapolation. Technical committees who have other information based on experience may use their dimensions.
- <sup>d</sup> The values given in brackets may be applied to reduce the **creepage distance** in case of using a rib (see [5.3.3.7\)](#page-29-2).

 $e$  Linear interpolation between two values of voltage is allowed (see [5.3.4,](#page-29-0) [5.3.5\)](#page-29-1).

See [5.3.3.8.](#page-29-3)
<span id="page-72-0"></span>

#### **Table F.6 – Test voltages for verifying clearances only at different altitudes**

NOTE 1 Explanations concerning the influencing factors (air pressure, altitude, temperature, humidity) with respect to electric strength of **clearances** are given in [4.7](#page-22-0) and altitude correction in [6.2.2.1.4](#page-34-0)

NOTE 2 When testing **clearances**, associated **solid insulation** will be subjected to the test voltage. As the impulse test voltage of [Table](#page-72-0) F.6 is increased with respect to the **rated impulse withstand voltage**, **solid**  insulation will be designed accordingly. This results in an increased impulse withstand capability of the solid **insulation**.





<sup>a</sup> Standard temperature of damp heat test appears in IEC 60068-2-78.

b Duration of the temperature change depends on the thermal time constant of the test specimen, see IEC 60068-2-14:2009.

#### <span id="page-73-0"></span>**Table F.8 – Dimensioning of clearances to withstand steady-state peak voltages, temporary overvoltages or recurring peak voltages <sup>b</sup>**

#### <span id="page-73-1"></span>**Table F.9 – Additional information concerning the dimensioning of clearances to avoid partial discharge**



NOTE If **clearances** are stressed with **steady-state peak voltages** of 2,5 kV and above, dimensioning according to the breakdown values in [Table F.8](#page-73-0) cannot provide operation without corona (**partial discharges**), especially for **inhomogeneous fields**. In order to provide corona-free operation, it is either preferrable to use larger **clearances**, as given in [Table F.9,](#page-73-1) or to improve the field distribution.

- <sup>a</sup> The **clearances** for other voltages are obtained by interpolation.
- <sup>b</sup> See [Figure 1](#page-17-0) for **steady-state peak voltage** and **recurring peak voltage**.
- <sup>c</sup> These values are based on experimental data obtained at atmospheric pressure.

NOTE If **clearances** are stressed with **steadystate peak voltages** of 2,5 kV and above, dimensioning according to the breakdown values in [Table F.8](#page-73-0) cannot provide operation without corona (**partial discharges**), especially for inhomogeneous fields. In order to provide corona-free operation, it is either preferrable to use larger **clearances**, as given in [Table F.9,](#page-73-1) or to improve the field distribution.

- <sup>a</sup> The **clearances** for other voltages are obtained by interpolation.
- <sup>b</sup> See [Figure 1](#page-17-0) for **steady-state peak voltage** and **recurring peak voltage**.
- <sup>c</sup> Dimensioning without **partial discharge** is not possible under **inhomogeneous field** conditions.



### **Table F.10 – Altitude correction factors for clearance correction**

## **Annex G**

(informative)

## **Determination of clearance distances according to [5.2](#page-23-0)**



**Figure G.1 – Determination of clearance distances according to [5.2](#page-23-0)** *(1 of 2)* 



NOTE 1 This flow-chart is only informative to improve the understanding of 5.2. Th[e ma](#page-23-0)nd[atory re](#page-25-0)quirement is given in 5.2.

NOTE 2 Dimensioning of **functional insulation** can be determined in a similar way considering 5.2 and 5.2.4.

NOTE 3 When the requirements of IEC 60664-3 Type 1 protection are met, **pollution degree** 1 in IEC 60664-1 is applicable.

**Figure G.1 – Determination of clearance distances according to 5.2** *(2 of 2)*

## **Annex H**

(informative)

## **Determination of creepage distances according to [5.3](#page-26-0)**



**Figure H.1 – Determination of creepage distances according to [5.3](#page-26-0)** *(1 of 2)*



NOTE 1 This flow-chart is only informative to improve the understanding of [5.3.](#page-26-0) The mandadoty requirement is given in [5.3.](#page-26-0)

NOTE 2 Dimensioning of **functional insulation** can be determined in a similar way considering [5.3](#page-26-0) and [5.3.4.](#page-29-0) NOTE 3 When the requirements of IEC 60664-3 Type 1 protection are met, **pollution degree** 1 in IEC 60664-1 is applicable.

#### **Figure H.1 – Determination of creepage distances according to [5.3](#page-26-0)** *(2 of 2)*

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